



## FEATURES

Adopt advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

## APPLICATIONS

- Solar inverters
- LCD/LED/PDP TV
- Telecom/Server Power supplies
- AC-DC Power Supply

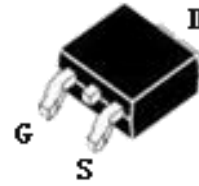
## MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum, 10s per JESD 22-B106

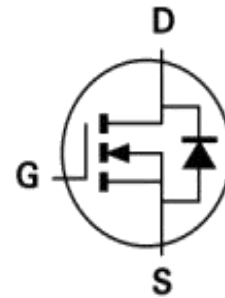
## MAIN CHARACTERISTICS

$I_D$	11A
$V_{DSS}$	650V
$R_{DS(ON)-typ} V_{GS}=10V$	340mΩ

## PACKAGE



TO-252



## Product specification classification

Part Number	Package	Mode Name	Pack
LX252F11N65	TO-252	LX252F11N65	Tube



**Maximum Ratings at  $T_c=25^\circ\text{C}$  unless otherwise specified**

Characteristics	Symbol	Value	Unit
		252	
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continue Drain Current	$I_b$	11	A
Pulsed Drain Current (Note1)	$I_{DM}$	31.8	A
Power Dissipation	$P_D$	84.5	W
Single Pulse Avalanche Energy (Note1)	$E_{AS}$	220	mJ
Operating Temperature Range	$T_J$	-50 to +150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-50 to +150	$^\circ\text{C}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.48	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62	$^\circ\text{C/W}$

Note1:Pulse test: 300  $\mu\text{s}$  pulse width, 2 % duty cycle

**Electrical Characteristics at  $T_c=25^\circ\text{C}$  unless otherwise specified**

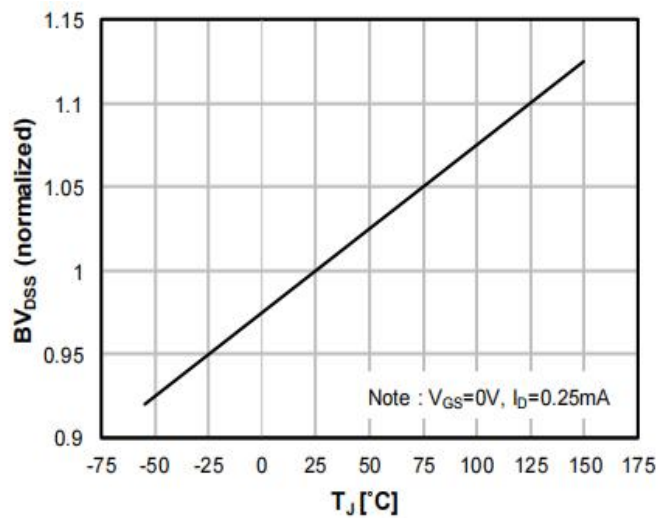
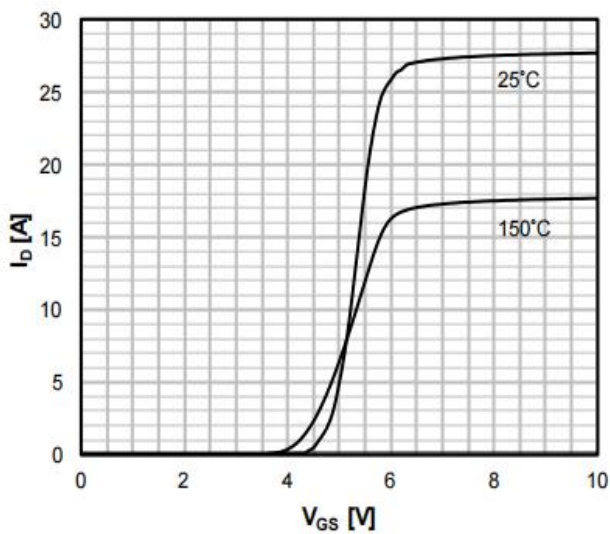
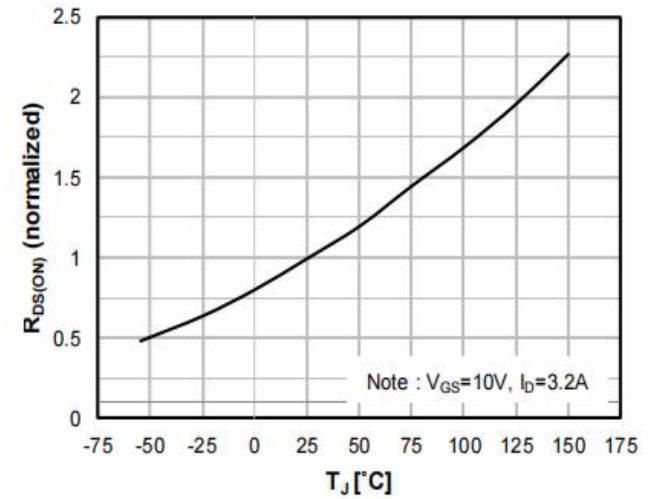
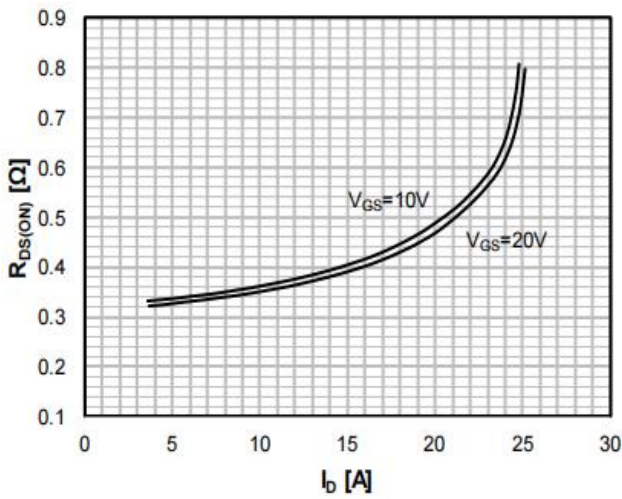
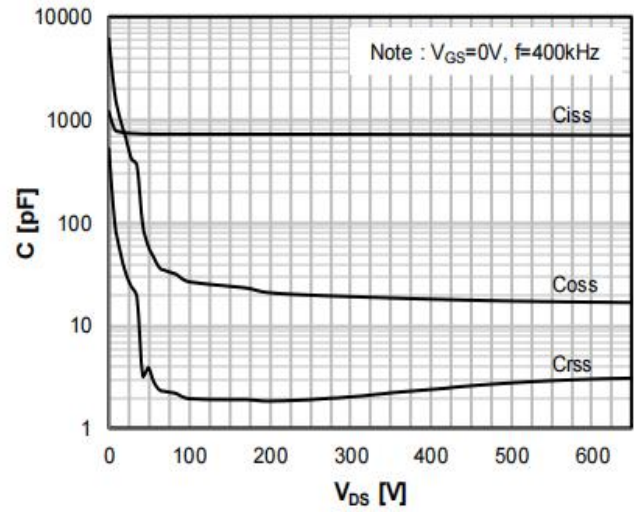
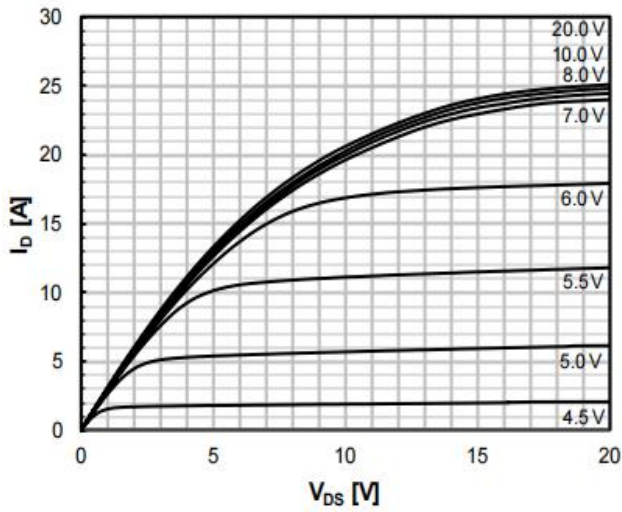
Characteristics	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	$BV_{DSS}$	650	-	-	V
Drain-Source Leakage Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	$I_{DSS}$	-	-	1	$\mu\text{A}$
Gate Leakage Current	$V_{GS} = \pm 30\text{ V}, V_{DS} = 0\text{ V}$	$I_{GSS}$	-	-	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	$V_{GS(th)}$	2	-	4	V
Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=1\text{A}$	$R_{DS(on)}$	-	340	380	$\text{m}\Omega$
Input Capacitance	$V_{DS}=50\text{V}, V_{GS}=0\text{V}, f=400\text{KHz}$	$C_{iss}$	-	747	-	pF
Output Capacitance		$C_{oss}$	-	55	-	pF
Reverse Transfer Capacitance		$C_{rss}$	-	3.3	-	pF
Turn-on Delay Time(Note2)	$V_{DD} = 325\text{V}, I_D = 10.6\text{ A}, V_{GS} = 10\text{V}, R_G = 25\Omega$	$t_{d(ON)}$	-	18	-	ns
Rise Time(Note2)		$t_r$	-	31	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	65	-	ns
Fall Time(Note2)		$t_f$	-	28	-	ns
Total Gate Charge(Note2)	$V_{DS} = 520\text{V}, V_{GS} = 10\text{V}, I_D = 10.6\text{ A}$	$Q_G$	-	20	-	nC
Gate to Source Charge(Note2)		$Q_{GS}$	-	3.7	-	nC
Gate to Drain Charge(Note2)		$Q_{GD}$	-	9	-	nC

**Source-Drain Diode Characteristics at  $T_a=25^\circ\text{C}$  unless otherwise specified**

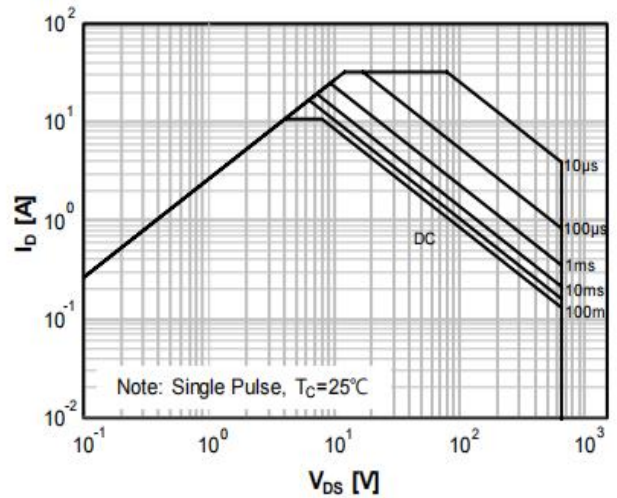
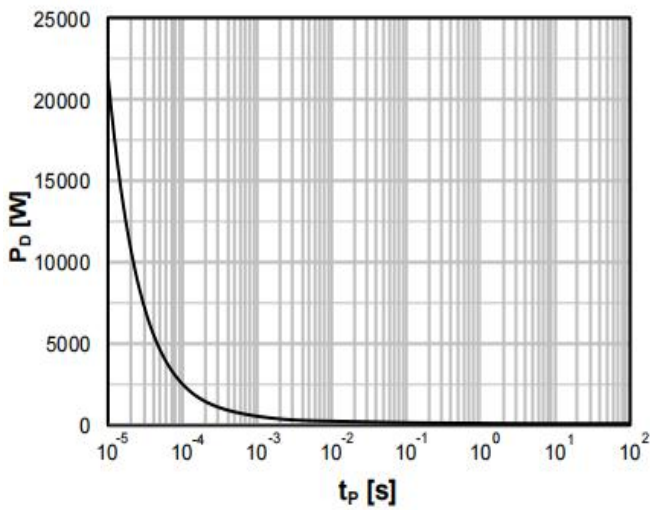
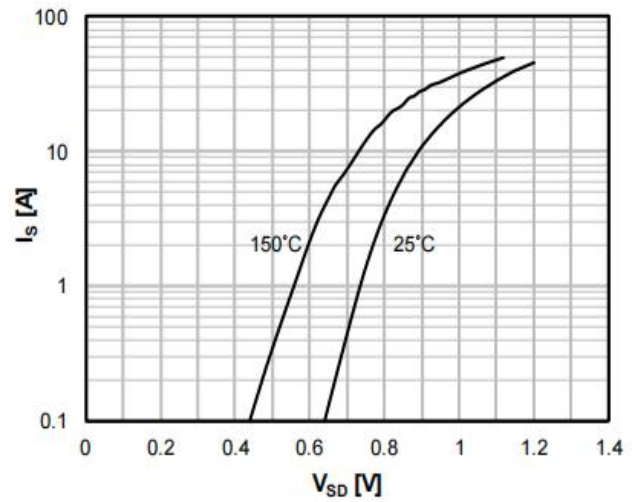
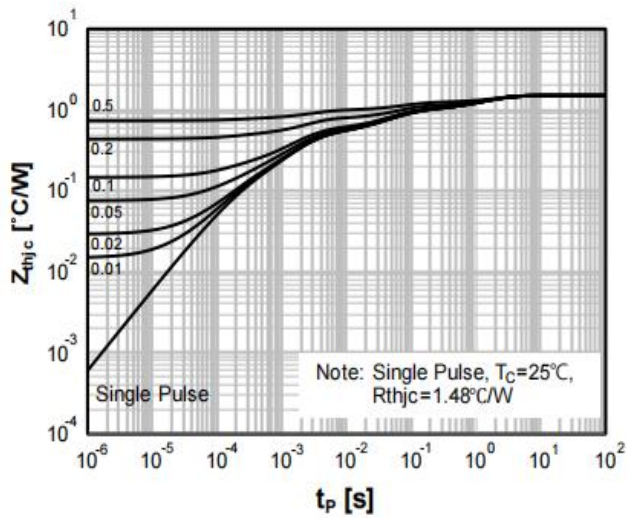
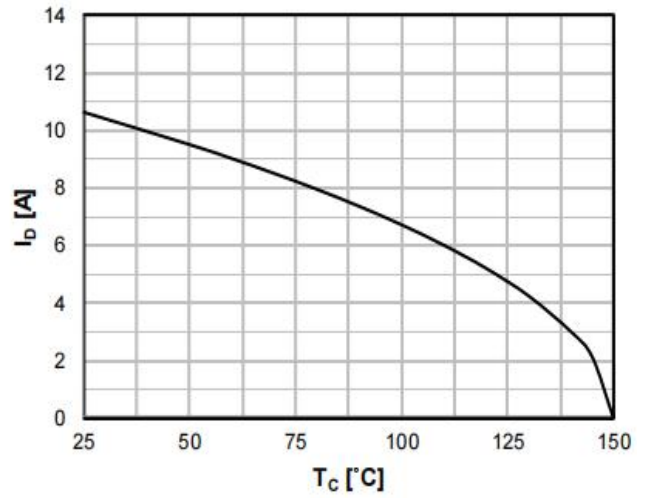
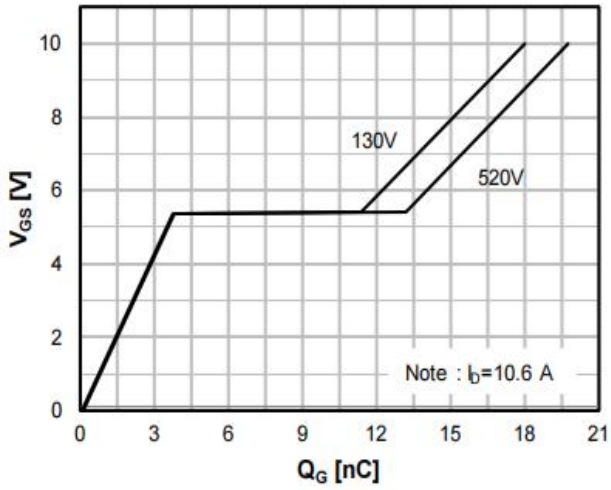
Characteristics	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Reverse recovery time	$I_{SD} = 10.6\text{A}, V_{DD} = 100\text{V}, di/dt = 100\text{A/us}$	$t_{rr}$	-	323	-	ns
Reverse recovery current		$I_{rr}$	-	17.5	-	A
Reverse recovery charge		$Q_{rr}$	-	2.8	-	$\mu\text{C}$
Drain-Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=10.6\text{A}, T_J=25^\circ\text{C}$	$V_{SD}$	-	1.4	-	V

Note2:Pulse test: 300  $\mu\text{s}$  pulse width, 2 % duty cycle

**RATINGS AND CHARACTERISTIC CURVES**



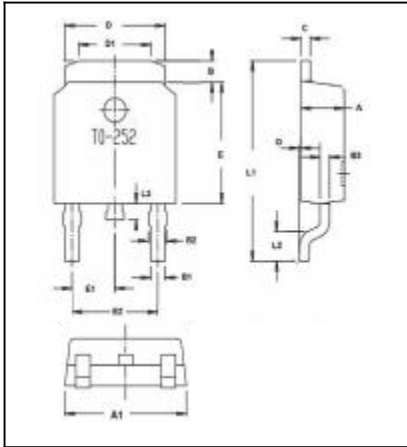
RATINGS AND CHARACTERISTIC CURVES





**Package Outline Dimensions millimeters**

**TO-252**



Dim.	Min.	Max.
A	2.1	2.5
A1	6.3	6.9
B	0.95	1.55
B1	0.6	0.8
B2	0.75	0.95
C	Typ0.5	
D	5.3	5.5
D1	3.65	4.05
E	5.8	6.4
E1	Typ2.3	
E2	Typ4.6	
O	0	0.15
L1	9	11
L2	Typ1.5	
L3	0.7	1
All Dimensions in millimeter		