



## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> (MAX)	Package	I <sub>D</sub> (MAX) T <sub>A</sub> = +25°C
30V	220mΩ @ V <sub>GS</sub> = 4.5 V	SOT323	1.5A
	300mΩ @ V <sub>GS</sub> = 2.5V		1A

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1)
- Halogen and Antimony Free. "Green" Device (Note 2)

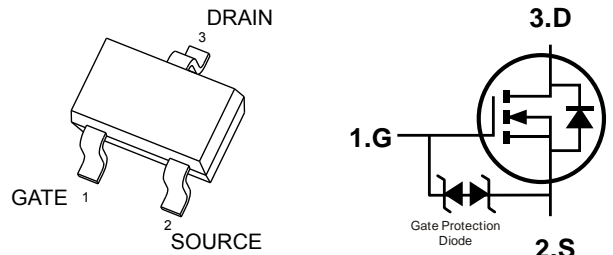
## Applications

- Motor Control
- Power Management Functions
- Load Switch

## Description

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Package



**SOT-323**

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	30	V	
Gate-Source Voltage	V <sub>GSS</sub>	±10	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	I <sub>D</sub>	Steady State T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	1000 900	mA
		t < 5s T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	1300 1000	mA
Maximum Continuous Body Diode Forward Current (Note 5)	I <sub>S</sub>	0.5	A	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	9.6	A	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.32	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	395	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	0.35	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	320	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	143	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



**Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)**

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 18V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.7	—	2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	160	220	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.5A
		—	240	300		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1.0A
Diode Forward Voltage	V <sub>SD</sub>	—	—	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 250mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	87	—	pF	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	17	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	12	—	pF	
Gate Resistance	R <sub>g</sub>	—	69.8	—	Ω	f = 1MHz, V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	0.9	—	nC	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	2.0	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	0.3	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	0.3	—	nC	
Turn-On Delay Time	t <sub>d(ON)</sub>	—	4.5	—	ns	
Turn-On Rise Time	t <sub>r</sub>	—	8.9	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, R <sub>G</sub> = 10Ω, I <sub>D</sub> = 100mA
Turn-Off Delay Time	t <sub>d(OFF)</sub>	—	30.3	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	15.6	—	ns	

- Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.  
6. Device mounted on 1"×1" FR-4 PCB with high coverage 2oz. Copper, single sided.  
7. Short duration pulse test used to minimize self-heating effect.  
8. Guaranteed by design. Not subject to product testing.

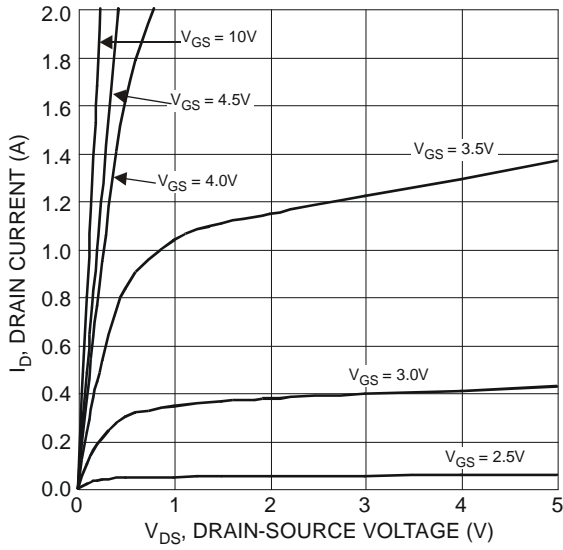


Figure 1 Typical Output Characteristic

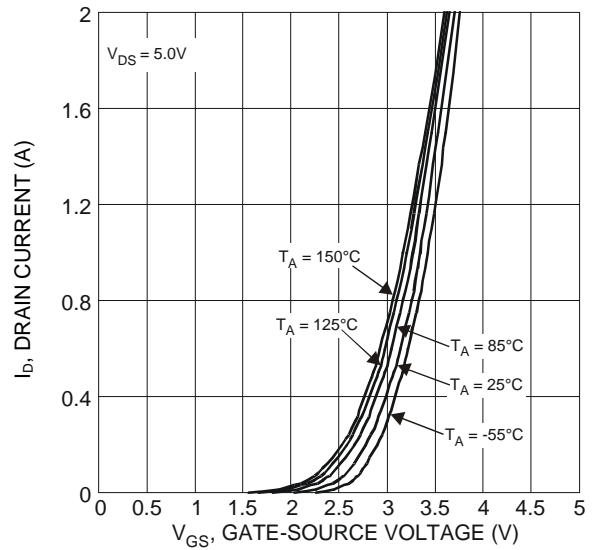


Figure 2 Typical Transfer Characteristics

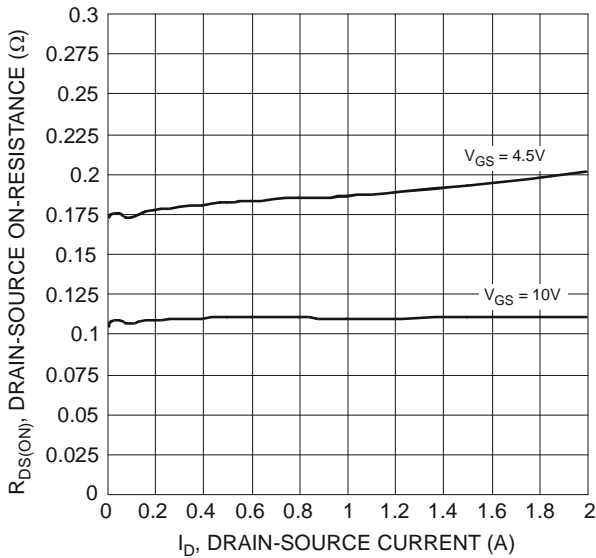


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

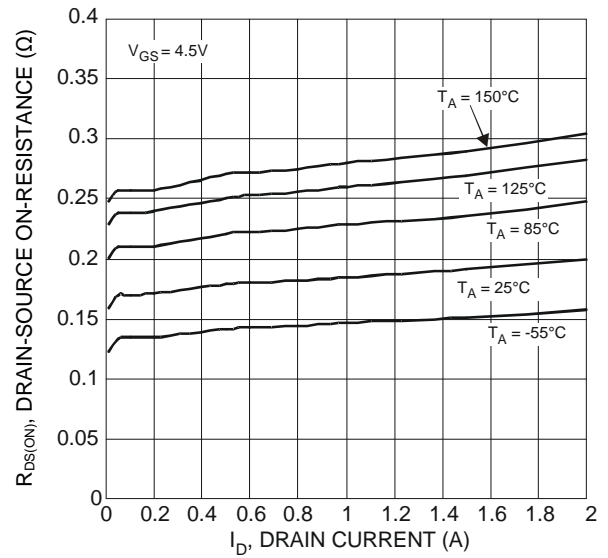


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

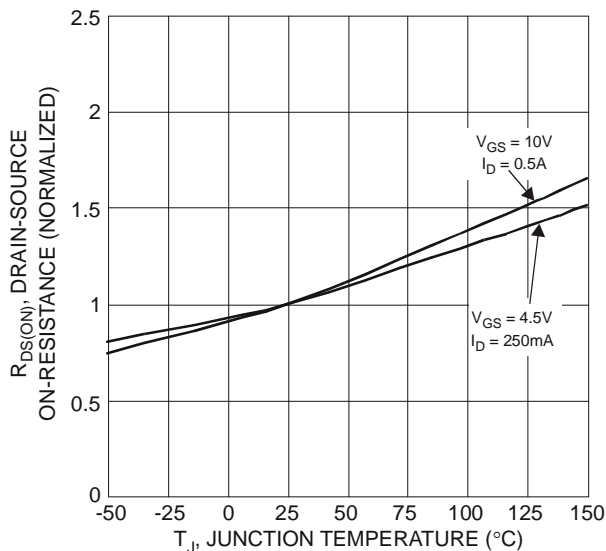


Figure 5 On-Resistance Variation with Temperature

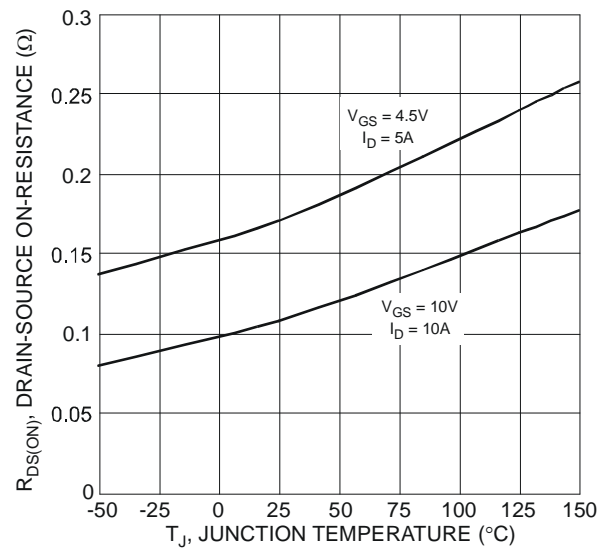


Figure 6 On-Resistance Variation with Temperature

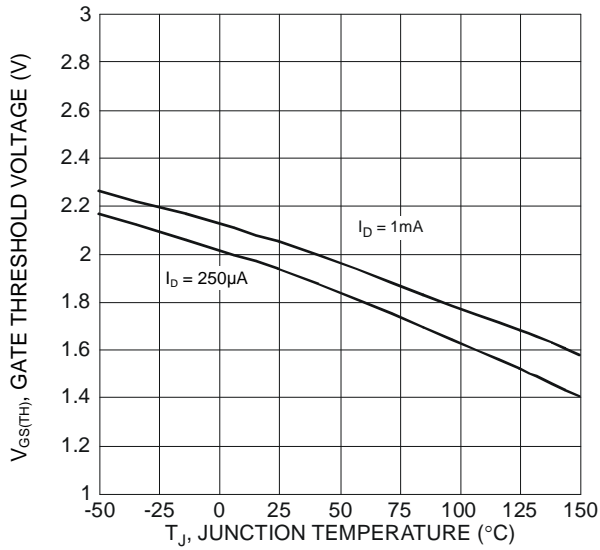


Figure 7 Gate Threshold Variation vs. Junction Temperature

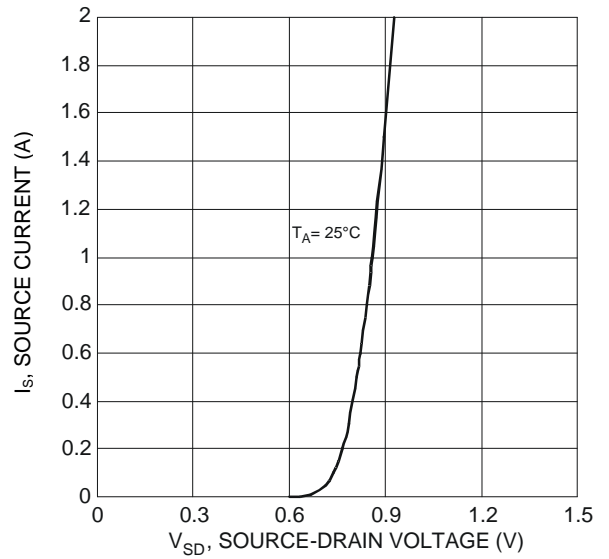


Figure 8 Diode Forward Voltage vs. Current

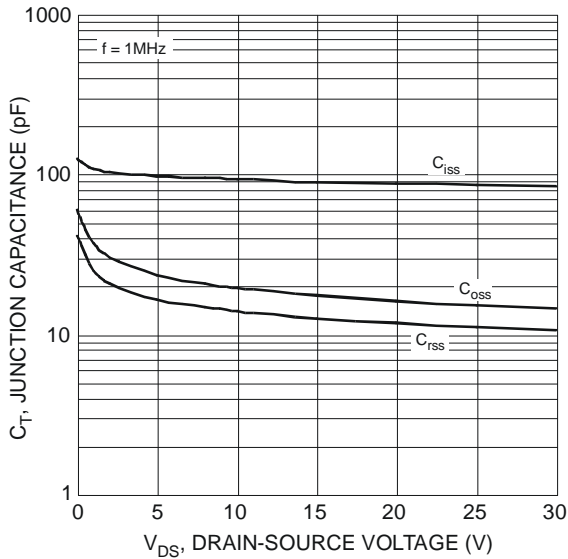


Figure 9 Typical Junction Capacitance

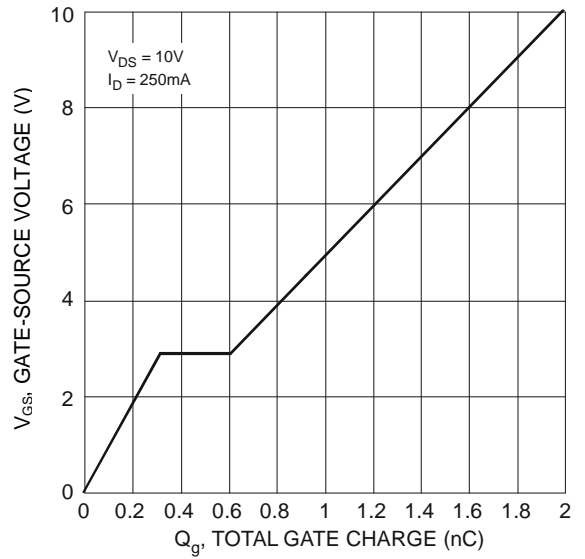


Figure 10 Gate Charge

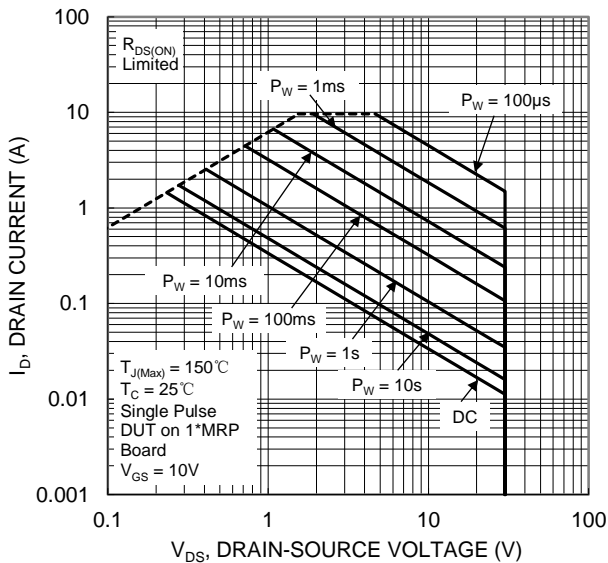
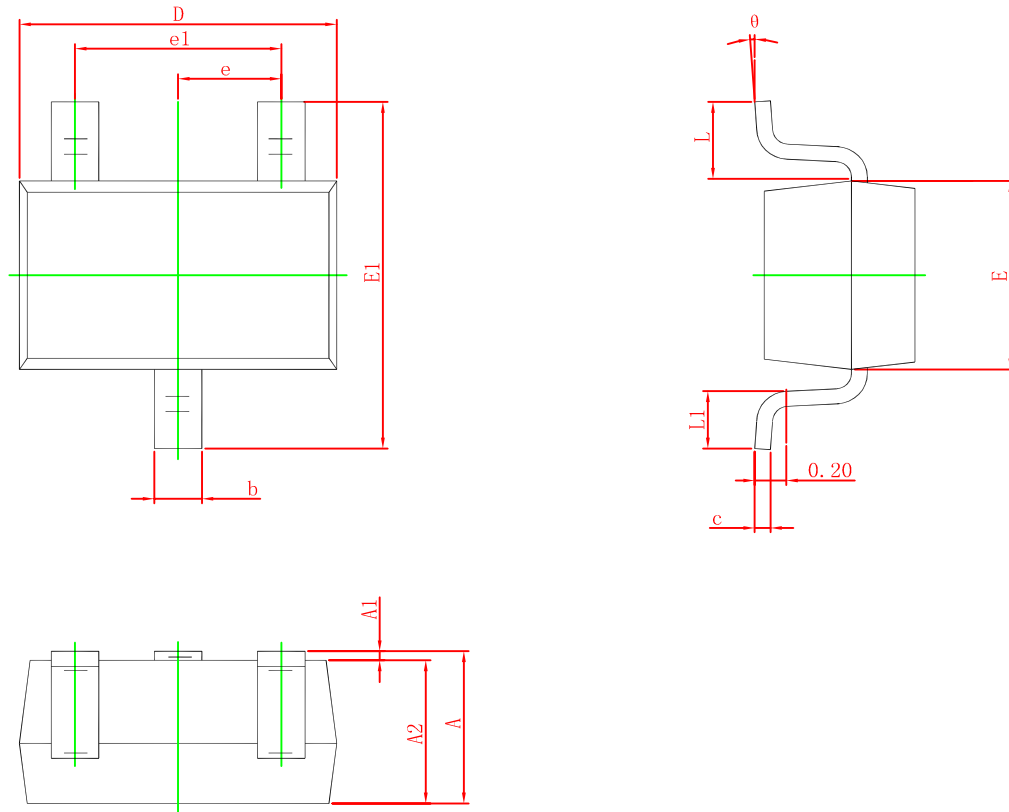


Figure 11 SOA, Safe Operation Area

**SOT-323 PACKAGE OUTLINE DIMENSIONS**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
$\theta$	0°	8°	0°	8°