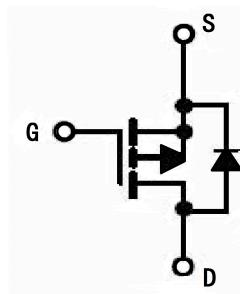


FEATURES

- $R_{DS(ON)} \leq 45m\Omega$ (35m Ω Typ.)
@ $V_{GS}=-4.5V$
- $R_{DS(ON)} \leq 0m\Omega$ (m Ω Typ.)
@ $V_{GS}=-2.5V$
- $R_{DS(ON)} \leq 90m\Omega$ (50m Ω Typ.)
@ $V_{GS}=-1.8V$

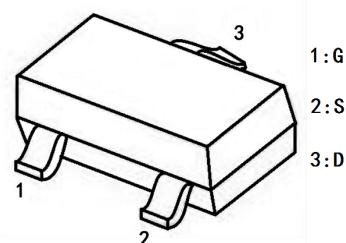


APPLICATIONS

- Load Switch for Portable Devices
- DC/DC Converter

MARKING

- MARKING:3415



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Maximum ratings ($T_a=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	
I_D	Continuous Drain Current	-4.1	A
I_{DM}	Pulsed Drain Current	-16	
P_D	Maximum Power Dissipation	0.83	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient($t \leq 5s$)	150	°C/W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55 ~+150	



MOSFET ELECTRICAL CHARACTERISTICS Ta=25 °C unless otherwise specified

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
Static						
V _{(BR)DSS}	Drain-source breakdown voltage	V _{GS} = 0V, I _D = -250μA	-20	-21.5		V
V _{GS(th)}	Gate-source threshold voltage	V _{DS} = V _{GS} , I _D = -250μA	-0.4	-0.7	-1	
I _{GSS}	Gate-source leakage	V _{DS} = 0V, V _{GS} = ±10V			±100	nA
I _{DSS}	Zero gate voltage drain current	V _{DS} = -16V, V _{GS} = 0V			-1	μA
R _{DS(on)}	Drain-source on-state resistance note1	V _{GS} = -4.5V, I _D = -3.5A		35	45	mΩ
		V _{GS} = -2.5V, I _D = -3A		38	70	
		V _{GS} = -1.8V, I _D = -2A		50	90	
V _{SD}	Body diode voltage	I _S = -3.3A		-0.9	-1.2	V
Dynamic ^{note2}						
C _{iss}	Input capacitance	V _{DS} = -4V, V _{GS} = 0V, f = 1MHz		740		pF
C _{oss}	Output capacitance			290		
C _{rss}	Reverse transfer capacitance			190		
Q _g	Total gate charge	V _{DS} = -4V, V _{GS} = -2.5V I _D = -4.1A		4.5	9	nC
Q _{gs}	Gate-source charge			1.2		
Q _{gd}	Gate-drain charge			1.6		
t _{d(on)}	Turn-on delay time	V _{DS} = -4V, R _L = 1.2Ω, I _D = -3.3A, V _{GEN} = -4.5V, R _G = 1Ω		13	20	nS
t _r	Rise time			35	53	
t _{d(off)}	Turn-off delay time			32	48	
t _f	Fall time			10	20	

Notes: 1. Pulse Test : Pulse Width < 300μs, Duty Cycle ≤2%.

2 . Guaranteed by design, not subject to production testing.

Typical Performance Characteristics

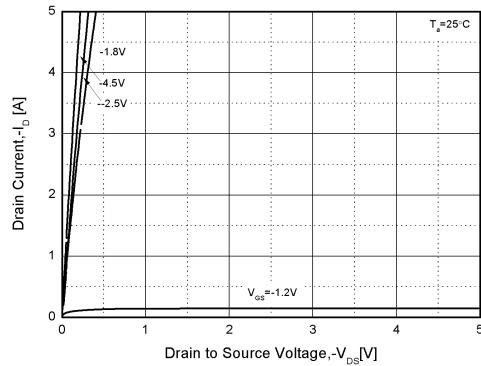


Figure1. Output Characteristics

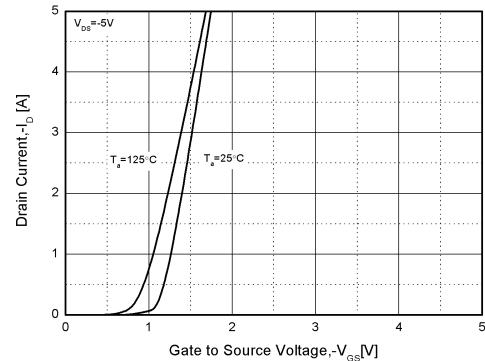


Figure2. Transfer Characteristics

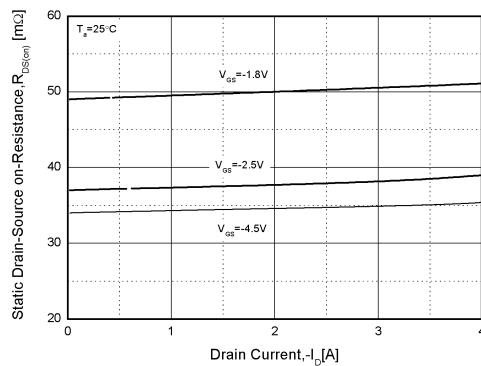


Figure3. R_{DSON} -Drain Current

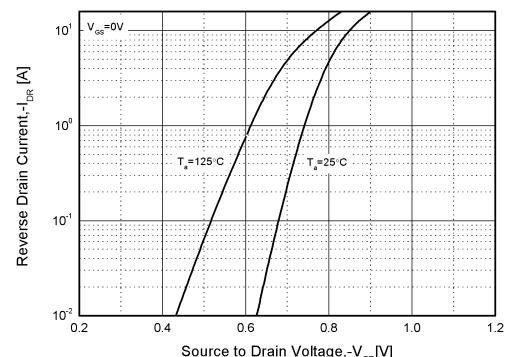


Figure4. Typical Source-Drain Diode Forward Voltage

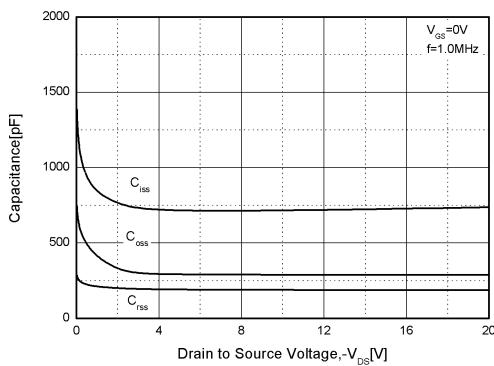


Figure5. Capacitance Characteristics

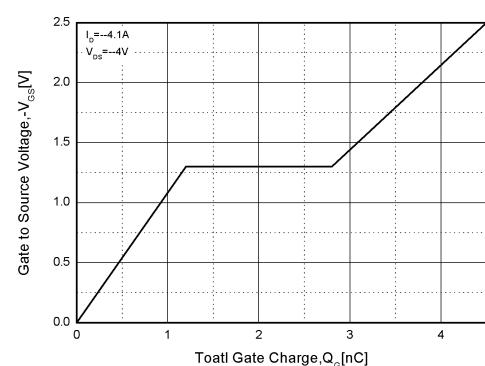


Figure6. Gate Charge

Typical Performance Characteristics (cont.)

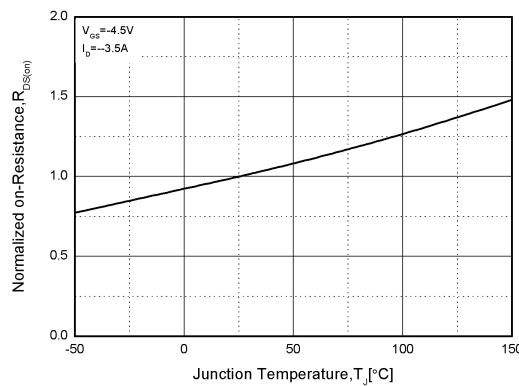
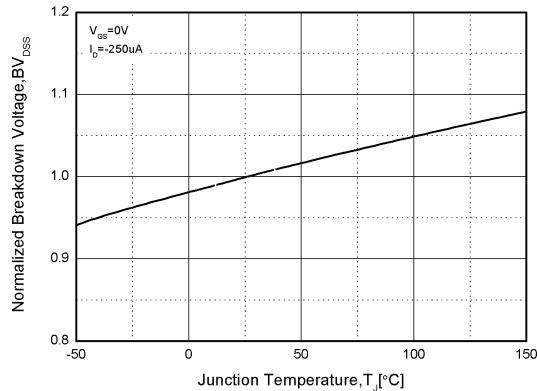
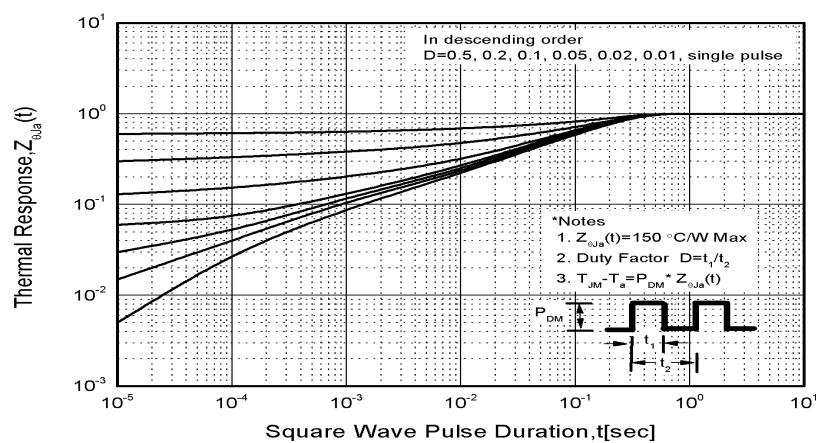
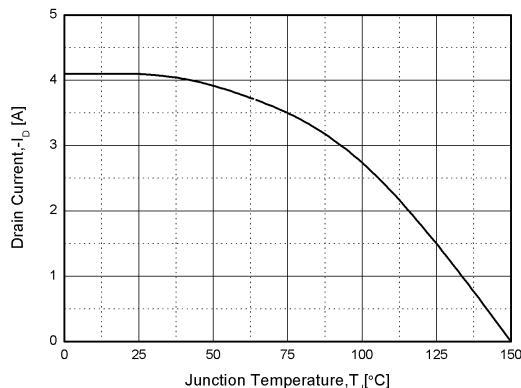
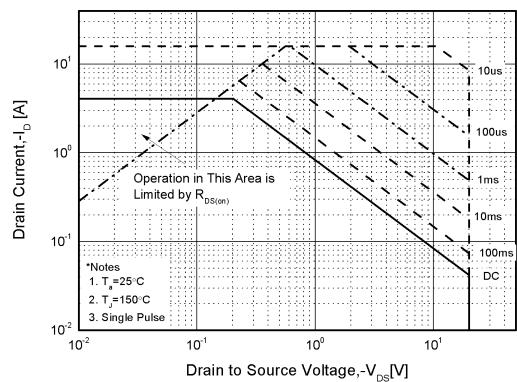


Figure 7. Normalized Breakdown Voltage vs. Temperature

Figure 8. Normalized on Resistance vs. Temperature



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

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