

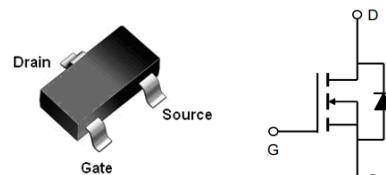
Features

- Low $R_{DS(on)}$ @ $V_{GS}=4.5V$
- 3.3V Logic Level Control
- N Channel SOT23 Package
- Pb-Free, RoHS Compliant

Applications

- Load Switch for
- DC/DC Converter
- Switching Circuits
- LED Driver

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ Typ}$	$I_D \text{ Max}$
20V	48mΩ @ 4.5V	3.2A
	55mΩ @ 3.3V	



SOT23

Order Information

Product	Package	Marking	Packing	Min Unit Quantity
LX2302S	SOT23	A2sHB	3000PCS/Reel	3000PCS

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Parameter	Symbol	Rating	Unit
Common Ratings ($T_A=25^\circ\text{C}$ Unless Otherwise Noted)			
Gate-Source Voltage	V_{GS}	± 10	V
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	20	V
Maximum Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-50 to 150	°C
Mounted on Large Heat Sink			
Pulse Drain Current Tested①	I_{DM}	12.8	A
Continuous Drain Current	I_D ($T_A=25^\circ\text{C}$)	3.2	A
	I_D ($T_A=70^\circ\text{C}$)	2.5	
Maximum Power Dissipation	P_D ($T_A=25^\circ\text{C}$)	1	W
	P_D ($T_A=70^\circ\text{C}$)	0.8	
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	125	°C/W



Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ\text{C}$)	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_A=125^\circ\text{C}$)	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	0.4	0.6	1.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$	--	48	60	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=3.3\text{V}$, $I_D=2\text{A}$	--	55	70	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=2.5\text{V}$, $I_D=1\text{A}$	--	66	80	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	160	--	pF
C_{oss}	Output Capacitance		--	30	--	pF
C_{rss}	Reverse Transfer Capacitance		--	25	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}$, $I_D=3\text{A}$, $V_{\text{GS}}=5\text{V}$	--	4.0	--	nC
Q_{gs}	Gate Source Charge		--	0.4	--	nC
Q_{gd}	Gate Drain Charge		--	1.2	--	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn on Delay Time	$V_{\text{DD}}=10\text{V}$, $I_D=2\text{A}$, $R_G=3.3\Omega$, $V_{\text{GS}}=4.5\text{V}$	--	8	--	ns
t_r	Turn on Rise Time		--	30	--	ns
$t_{\text{d}(\text{off})}$	Turn Off Delay Time		-	19	--	ns
t_f	Turn Off Fall Time		--	28	--	ns
Source Drain Diode Characteristics						
I_{SD}	Source drain current(Body Diode)	$T_A=25^\circ\text{C}$	--	--	1.5	A
V_{SD}	Forward on voltage②	$T_J=25^\circ\text{C}$, $I_{\text{SD}}=1\text{A}$, $V_{\text{GS}}=0\text{V}$	--	0.82	1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

Typical Characteristics

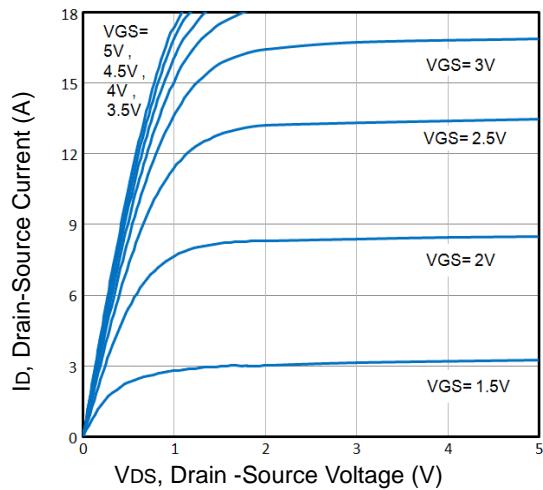


Fig1. Typical Output Characteristics

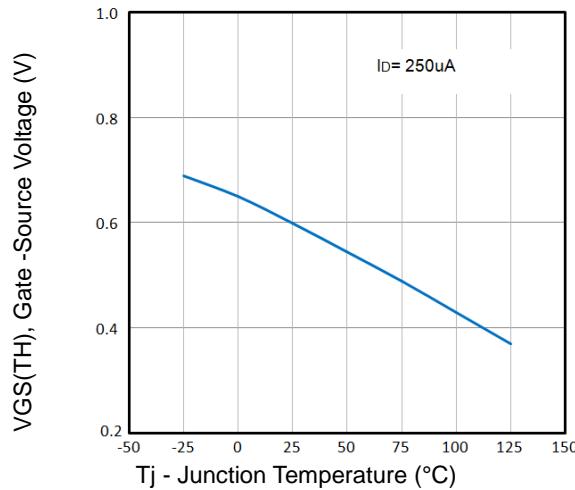


Fig2. Normalized Threshold Voltage Vs. Temperature

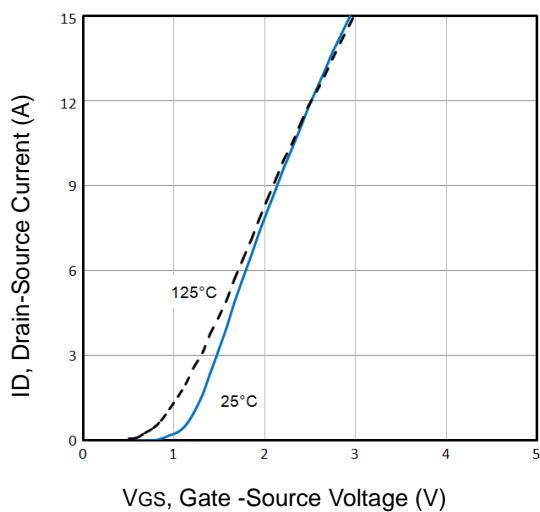


Fig3. Typical Transfer Characteristics

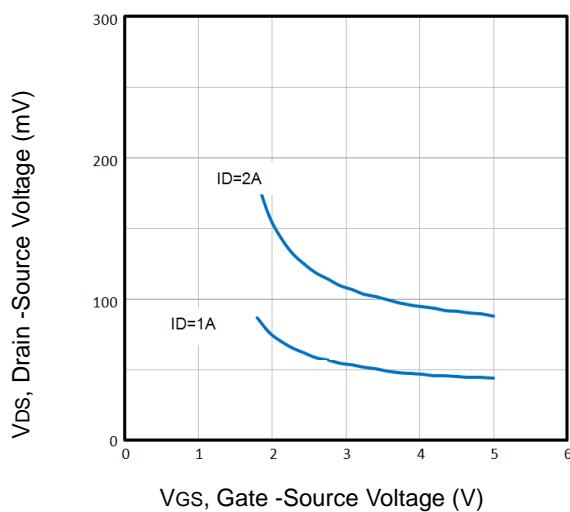


Fig4. Drain -Source Voltage vs Gate -Source Voltage

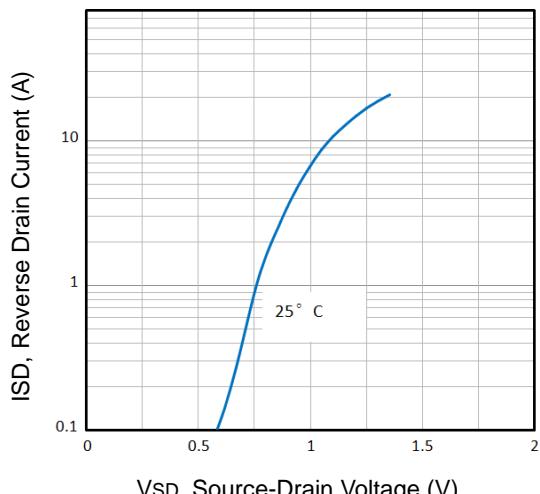


Fig5. Typical Source-Drain Diode Forward Voltage

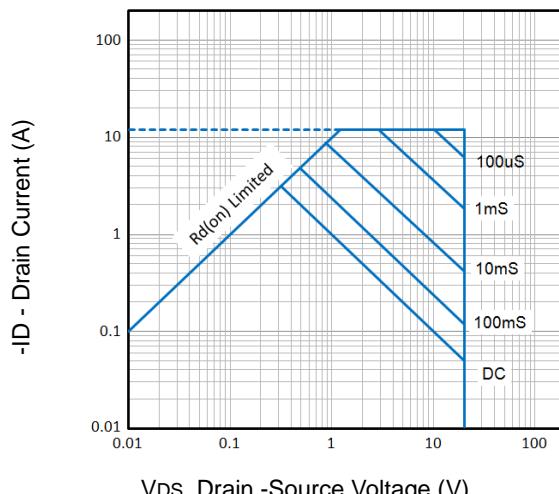


Fig6. Maximum Safe Operating Area

Typical Characteristics

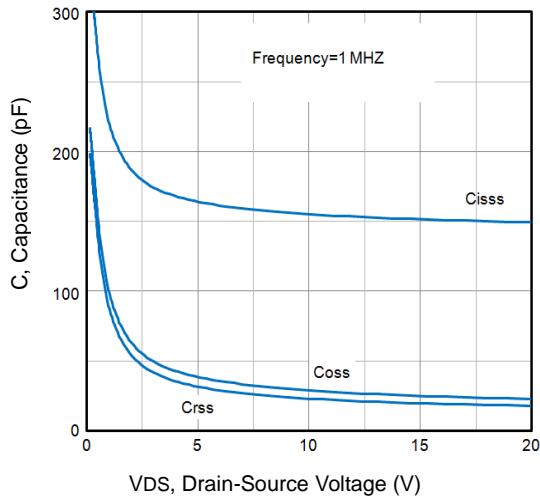


Fig7. Typical Capacitance Vs. Drain-Source Voltage

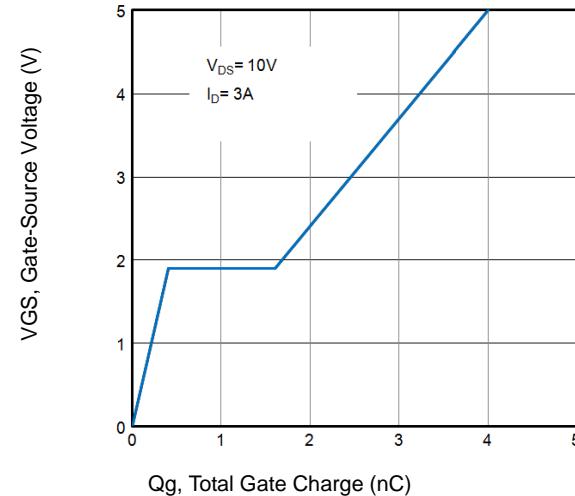


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

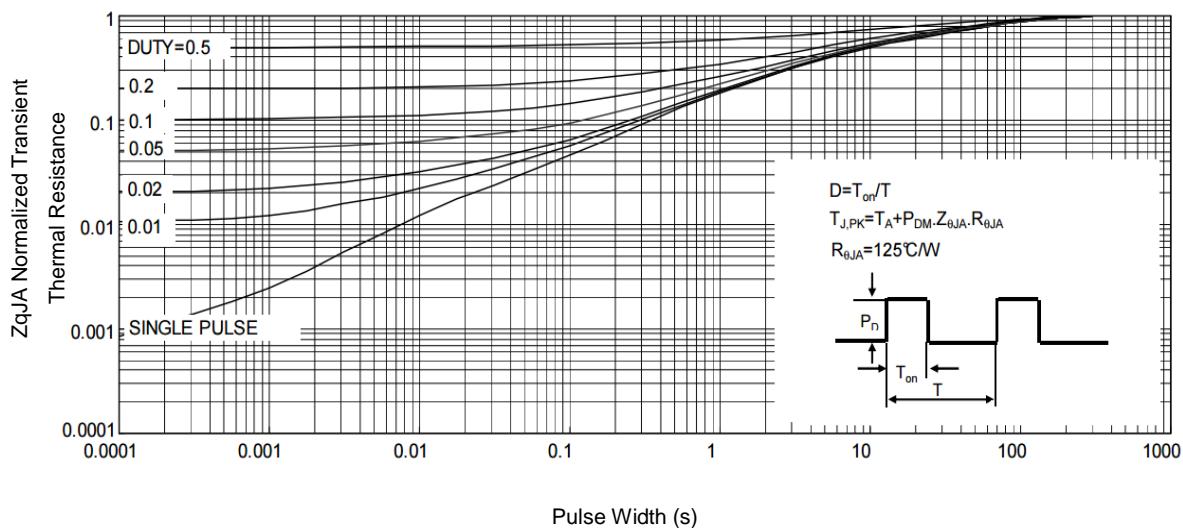


Fig9. Normalized Maximum Transient Thermal Impedance

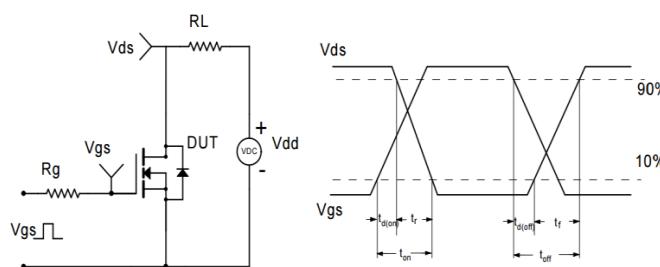
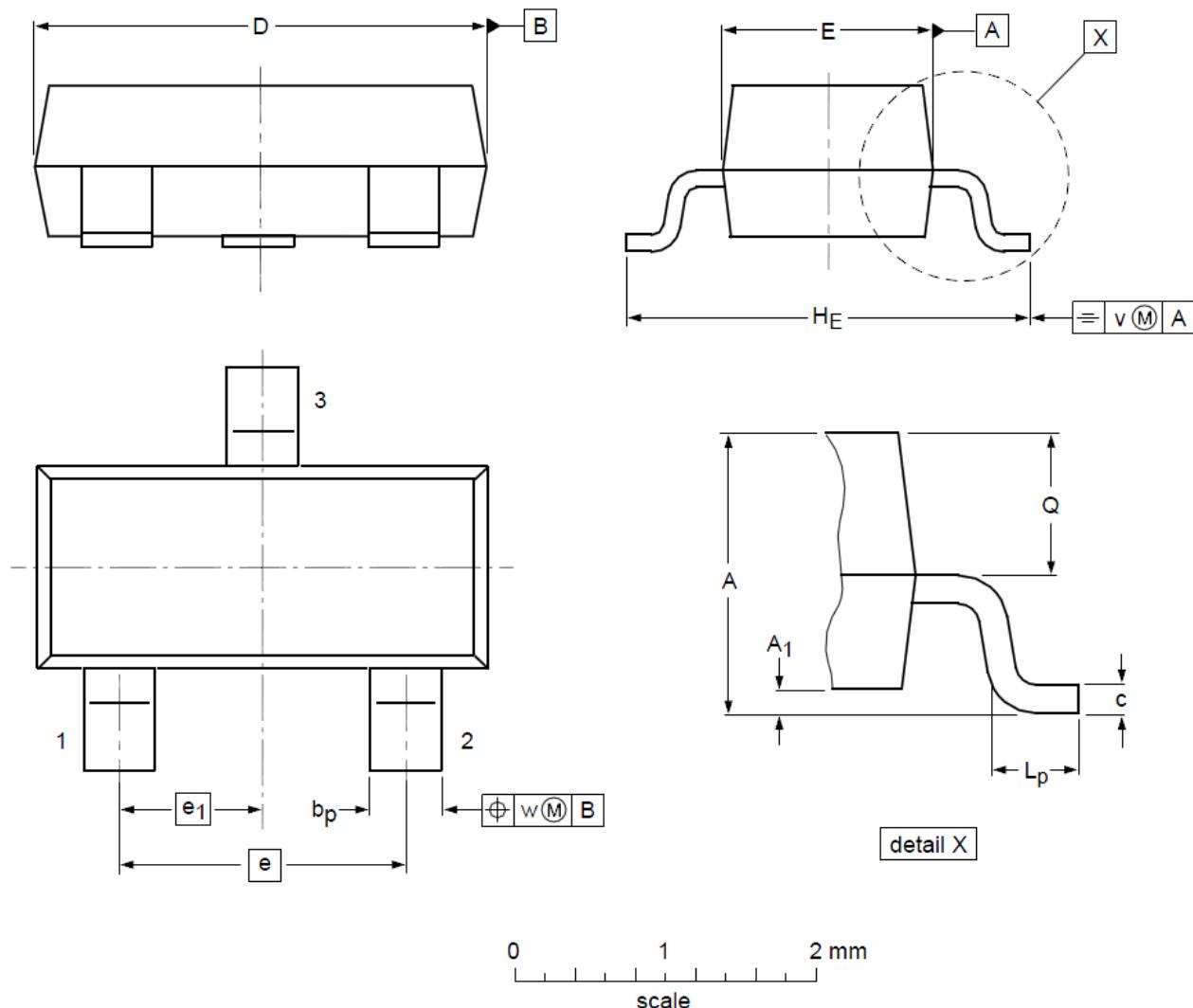


Fig10. Switching Time Test Circuit and waveforms

SOT23 Mechanical Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A₁	0.01	0.05	0.10
b_p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e₁	--	0.95	--
H_E	2.25	2.40	2.55	L_p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				