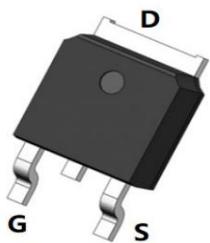


Features

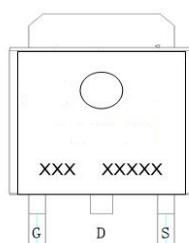
- 100V, 10A
- $R_{DS(ON)}$ Typ= 117mΩ @ V_{GS} = 10V
- $R_{DS(ON)}$ Typ= 126mΩ @ V_{GS} = 4.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge

Applications

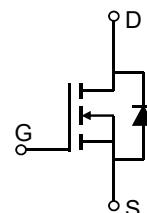
- Load Switch
- PWM Application
- Power Management



TO-252-3L



Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
LX252T10N100	LX252T10N100	TAPING	TO-252-3L	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value		Units
V_{DS}	Drain-to-Source Voltage		100		V
V_{GS}	Gate-to-Source Voltage		±20		V
I_D	Continuous Drain Current		$T_C = 25^\circ\text{C}$	10	A
			$T_C = 100^\circ\text{C}$	6	
I_{DM}	Pulsed Drain Current ⁽¹⁾		40		A
E_{AS}	Single Pulsed Avalanche Energy		12		mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	26.6		W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		4.7		°C/W
T_J , T_{STG}	Junction & Storage Temperature Range		-55 to 150		°C

Electrical Characteristics($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	2.0	2.5	V
$R_{DS(\text{ON})}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}, I_D = 5\text{A}$	-	117	152	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 3\text{A}$	-	126	164	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$	-	480	-	pF
C_{oss}	Output Capacitance		-	30	-	pF
C_{rss}	Reverse Transfer Capacitance		-	22	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 30\text{V}, I_D = 3\text{A}$	-	14.8	-	nC
Q_{gs}	Gate Source Charge		-	3	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	4.4	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$ $I_D = 3\text{A}, R_{\text{GEN}} = 3\Omega$	-	12	-	ns
t_r	Turn-On Rise Time		-	7.6	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	36	-	ns
t_f	Turn-Off Fall Time		-	9.2	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current	-	-	10	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	40	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 10\text{A}$	-	-	1.2	V

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_G=25\text{ohm}$, $L=0.5\text{mH}$, $I_{AS}=7\text{A}$

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

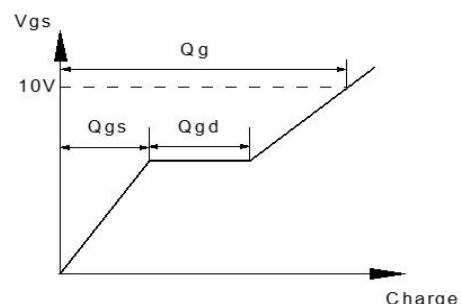
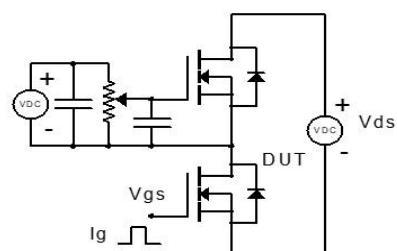


Figure 1: Gate Charge Test Circuit & Waveform

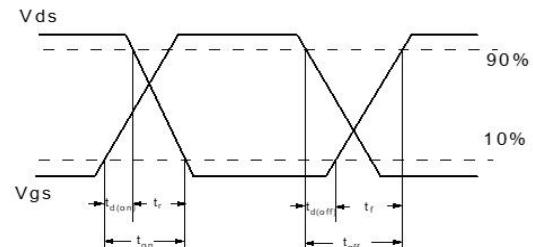
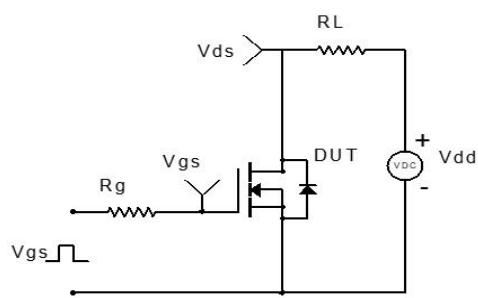


Figure 2: Resistive Switching Test Circuit & Waveform

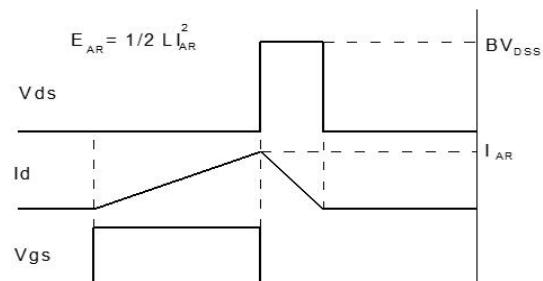
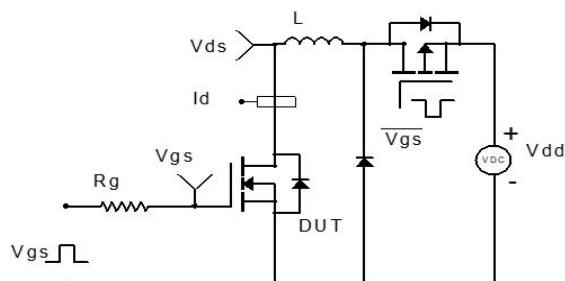


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

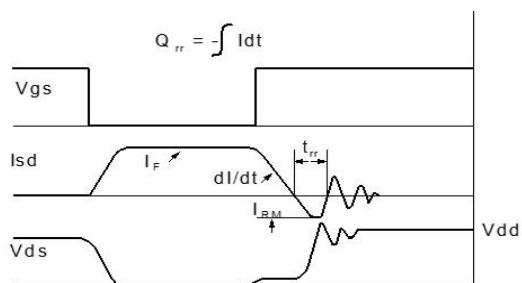
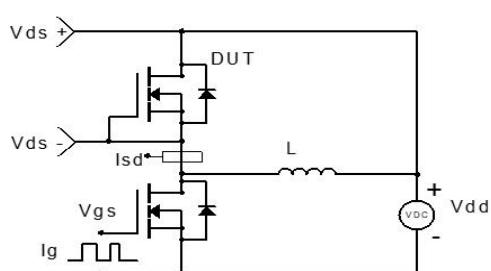
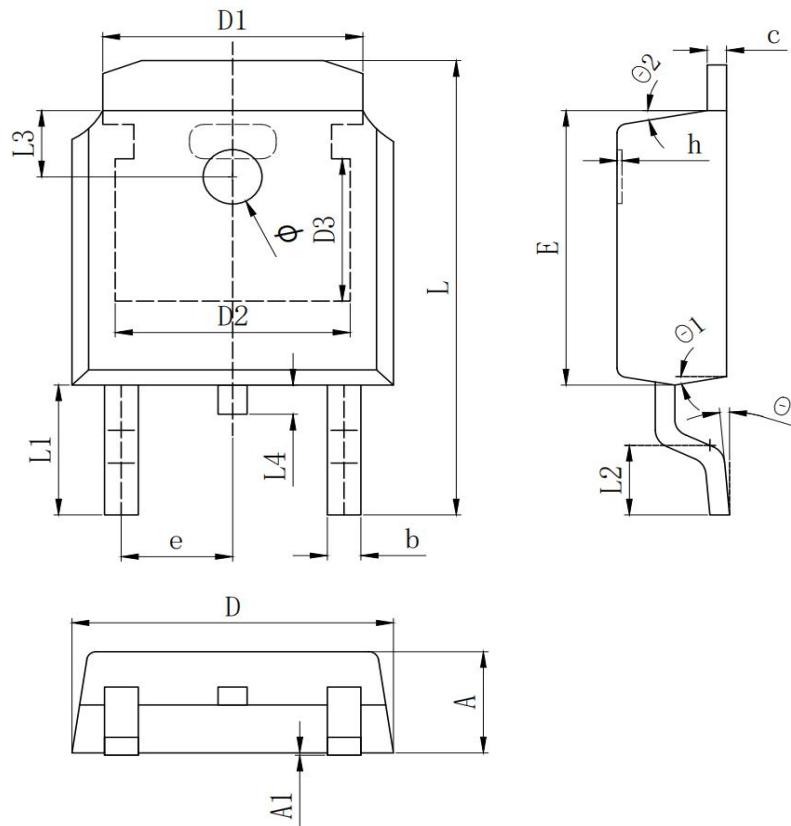


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-252-3L)



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334	REF	
D2	4.826	REF	
D3	3.166	REF	
E	6.000	6.100	6.200
e	2.286	TYP	
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888	REF	
L2	1.400	1.550	1.700
L3	1.600	REF	
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
Θ	0°		8°
Θ1	9° TYP		
Θ2	9° TYP		