

## FEATURE

- High density cell design for extremely low  $R_{DS(on)}$
- Rugged and Reliable

## APPLICATION

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

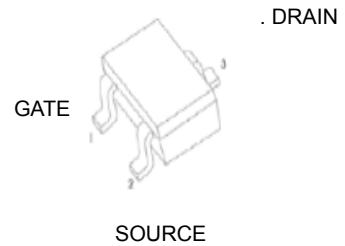
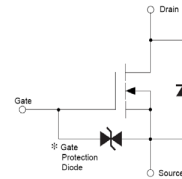
## MARKING

- MARKING:SS

## N-Channel 50-V(D-S) MOSFET

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
50V	3.5Ω@10V	220mA
	6Ω@4.5V	

## Package



## SOT-523

## Maximum ratings ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	50	V
Continuous Gate-Source Voltage	$V_{GSS}$	$\pm 20$	
Continuous Drain Current	$I_D$	0.22	A
Power Dissipation	$P_D$	0.30	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	$^\circ\text{C}/\text{W}$
Operating Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 ~ +150	



## MOSFET ELECTRICAL CHARACTERISTICS

Ta=25 °C unless otherwise specified

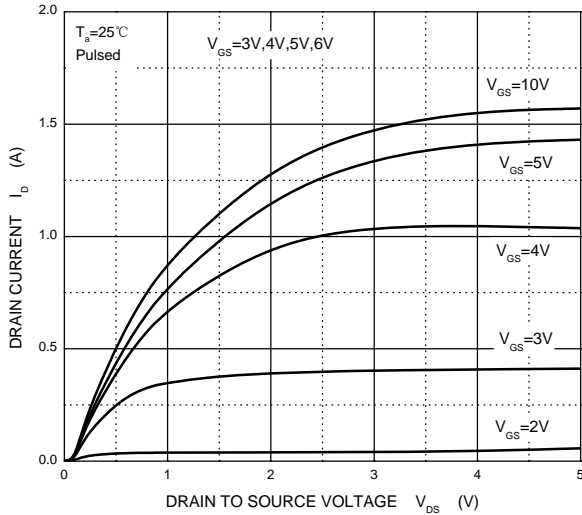
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	50			V
Gate-body leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 10$	$\mu A$
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 50V, V_{GS} = 0V$			0.5	$\mu A$
		$V_{DS} = 30V, V_{GS} = 0V$			100	nA
<b>On characteristics</b>						
Gate-threshold voltage (note 1)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 0.25mA$	0.80		1.50	V
Static drain-source on-resistance (note 1)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 0.22A$			3.50	$\Omega$
		$V_{GS} = 4.5V, I_D = 0.22A$			6	
Forward transconductance (note 1)	$g_{FS}$	$V_{DS} = 10V, I_D = 0.22A$	0.12			S
<b>Dynamic characteristics (note 2)</b>						
Input capacitance	$C_{iss}$	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$		27		pF
Output capacitance	$C_{oss}$			13		
Reverse transfer capacitance	$C_{rss}$			6		
<b>Switching characteristics</b>						
Turn-on delay time (note 1,2)	$t_{d(on)}$	$V_{DD} = 30V, V_{DS} = 10V, I_D = 0.29A, R_{GEN} = 6\Omega$			5	ns
Rise time (note 1,2)	$t_r$				18	
Turn-off delay time (note 1,2)	$t_{d(off)}$				36	
Fall time (note 1,2)	$t_f$				14	
<b>Drain-source body diode characteristics</b>						
Body diode forward voltage (note 1)	$V_{SD}$	$I_S = 0.44A, V_{GS} = 0V$			1.4	V

### Notes:

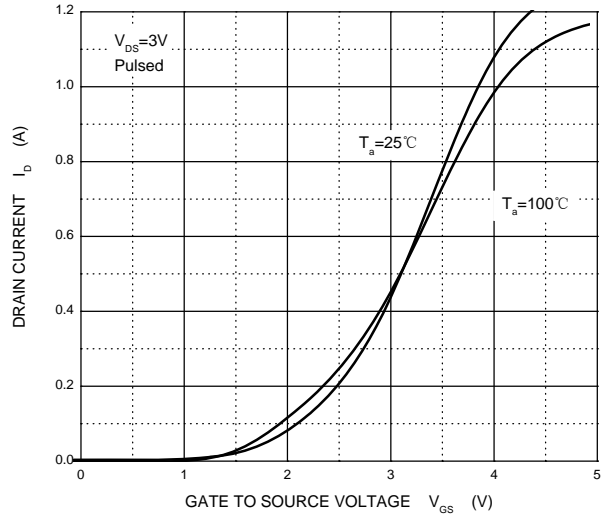
1. Pulse Test ; Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
2. These parameters have no way to verify.

Typical Characteristics

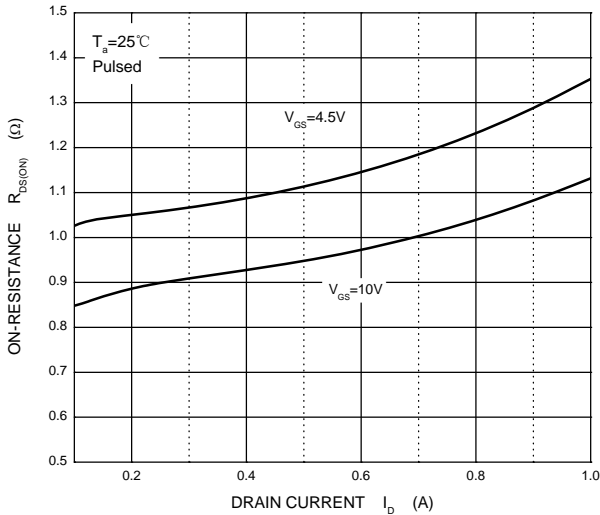
Output Characteristics



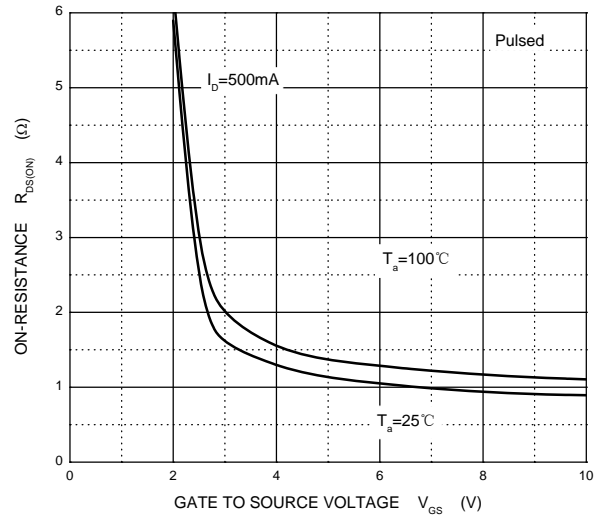
Transfer Characteristics



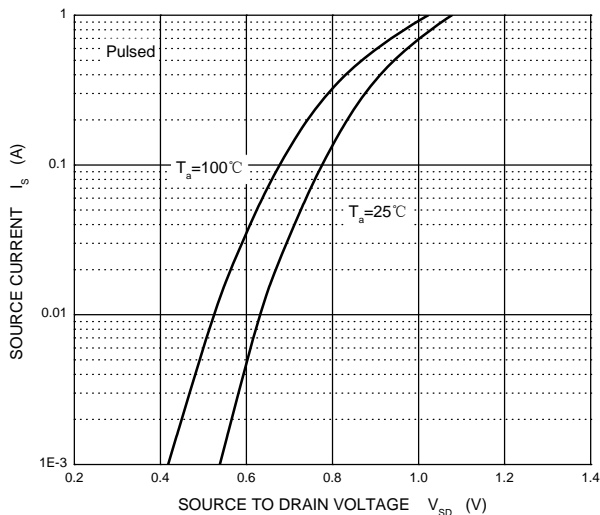
$R_{DS(ON)}$  —  $I_D$



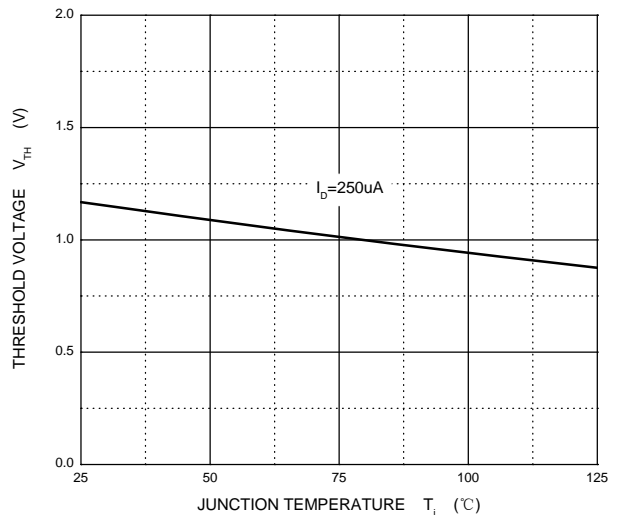
$R_{DS(ON)}$  —  $V_{GS}$



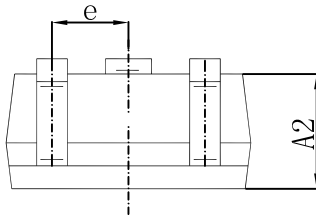
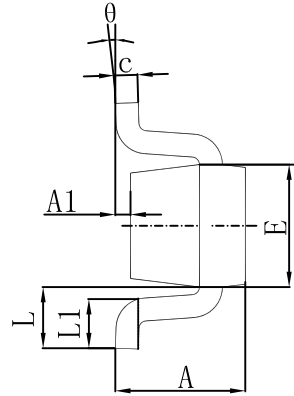
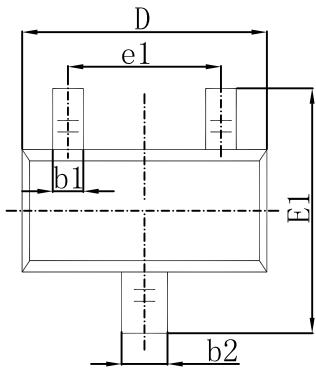
$I_S$  —  $V_{SD}$



Threshold Voltage

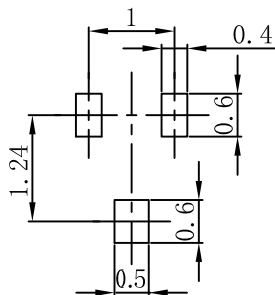


**SOT-523 Package Outline Dimensions**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	0.700	0.900	0.028	0.035
E1	1.450	1.750	0.057	0.069
e	0.500 TYP.		0.020 TYP.	
e1	0.900	1.100	0.035	0.043
L	0.400 REF.		0.016 REF.	
L1	0.260	0.460	0.010	0.018
theta	0°	8°	0°	8°

**SOT-523 Suggested Pad Layout**



- Note:
1. Controlling dimension: in millimeters.
  2. General tolerance: ±0.05mm.
  3. The pad layout is for reference purposes only.