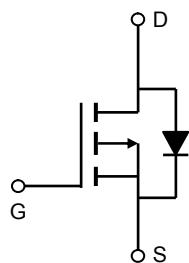
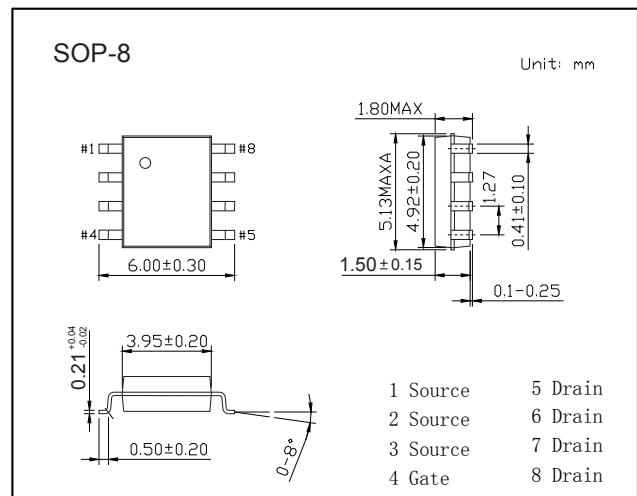


Features

- $V_{DS(V)} = -30V$
- $I_D = -10.5 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 13m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 25m\Omega (V_{GS} = -4.5 V)$



Package



Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	±20	
Continuous Drain Current	I_D	-10.5	A
		-8	
Pulsed Drain Current	I_{DM}	-60	
Avalanche Current	I_{AR}	-17	
Repetitive Avalanche Energy	E_{AR}	60	mJ
Power Dissipation	P_D	3.1	W
		2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	40	°C/W
		75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24	°C
Junction Temperature	T_J	150	
Junction Storage Temperature Range	T_{stg}	-55 to 150	



Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D = -250 \mu\text{A}, V_{GS} = 0\text{V}$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$			-1	uA
		$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}, T_J = 55^\circ\text{C}$			-5	
Gate-Body leakage current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-0.8		-2.5	V
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = -20\text{V}, I_D = -10\text{A}$		11		m Ω
		$V_{GS} = -20\text{V}, I_D = -10\text{A}, T_J = 125^\circ\text{C}$		12		
		$V_{GS} = -10\text{V}, I_D = -10\text{A}$		13		
		$V_{GS} = -4.5\text{V}, I_D = -5\text{A}$		25		
On state drain current	$I_{D(\text{ON})}$	$V_{GS} = -10\text{V}, V_{DS} = -5\text{V}$	-60			A
Forward Transconductance	g_{FS}	$V_{DS} = -5\text{V}, I_D = -10\text{A}$		22		S
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = -15\text{V}, f = 1\text{MHz}$		1130	1400	pF
Output Capacitance	C_{oss}			240		
Reverse Transfer Capacitance	C_{rss}			155		
Gate resistance	R_g	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$	1		8	Ω
Total Gate Charge (10V)	Q_g	$V_{GS} = -10\text{V}, V_{DS} = -15\text{V}, I_D = -10\text{A}$		18	24	nC
Total Gate Charge (4.5V)				9.5		
Gate Source Charge	Q_{gs}			5.5		
Gate Drain Charge	Q_{gd}			3.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = -10\text{V}, V_{DS} = -15\text{V}, R_L = 1.5\Omega, R_{GEN} = 3\Omega$		8.7		ns
Turn-On Rise Time	t_r			8.5		
Turn-Off Delay Time	$t_{d(off)}$			18		
Turn-Off Fall Time	t_f			7		
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -10\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		25	30	nC
Body Diode Reverse Recovery Charge	Q_{rr}			12		
Maximum Body-Diode Continuous Current	I_s				-3.5	A
Diode Forward Voltage	V_{SD}	$I_s = -1\text{A}, V_{GS} = 0\text{V}$			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using $<300 \mu\text{s}$ pulses, duty cycle 0.5% max.

Marking

Marking	4435
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