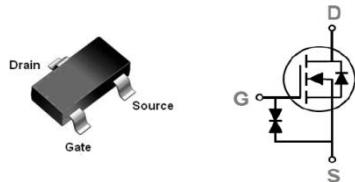


Features

- Low $R_{DS(on)}$ @ $V_{GS} = 10V$
- 5V Logic Level Control
- N Channel SOT23 Package
- HBM ESD Protection 2KV
- Pb-Free, RoHS Compliant

Dimensions & Symbol



SOT23

Applications

- Logic level translators
- High-speed line drivers
- Low-side load switch
- Switching circuits
- Relay driver

$V_{(BR)DSS}$	$R_{DS(ON)}$ Typ	I_D Max
60V	1.9Ω @ 10V	0.3A
	2.5Ω @ 4.5V	

Absolute maximum ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability

Symbol	Parameter	Rating	Unit
Common Ratings (TA=25°C Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	±20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	60	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-50 to 150	°C

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested①	$T_A = 25^\circ C$	1.2	A
I_D	Continuous Drain Current($V_{GS}=4.5V$)	$T_A = 25^\circ C$	300	mA
		$T_A = 70^\circ C$	240	
P_D	Maximum Power Dissipation	$T_A = 25^\circ C$	0.3	W
		$T_A = 70^\circ C$	0.2	
$R_{θJA}$	Thermal Resistance Junction-Ambient		400	°C/W



Symbol	Parameter	Condition 0.01	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated)						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ C$)	$V_{DS}=60V, V_{GS}=0V$	--	--	1	pA
	Zero Gate Voltage Drain Current($T_A=125^\circ C$)	$V_{DS}=48V, V_{GS}=0V$	--	--	100	uA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 10	uA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.5	V
$R_{(DS)ON}$	Drain-Source On-State Resistance②	$V_{GS}=10V, I_D=0.3A$	--	1.9	4	Q
$R_{(DS)ON}$	Drain-Source On-State Resistance②	$V_{GS}=4.5V, I_D=0.3A$	--	2.5	5	Q
Dynamic Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{DS}=30V, V_{GS}=0V, f=1MHz$	--	16	--	pF
C_{oss}	Output Capacitance		--	3.8	--	pF
C_{rss}	Reverse Transfer Capacitance		--	0.6	--	pF
Q_g	Total Gate Charge	$V_{DS}=30V, I_D=0.5A, V_{GS}=10V$	--	0.72	--	nC
Q_{gs}	Gate Source Charge		--	0.15	--	nC
Q_{gd}	Gate Drain Charge		--	0.22	--	nC
Switching Characteristics						
$t_d(ON)$	Turn on Delay Time	$V_{DD}=30V, I_D=0.3A, R_G=3.3Q, V_{GS}=10V$	--	5	--	ns
t_r	Turn on Rise Time		--	3.3	--	ns
$t_d(off)$	Turn Off Delay Time		-	18	--	ns
t_f	Turn Off Fall Time		--	5.2	--	ns
Source Drain Diode Characteristics						
I_{SD}	Source drain current(Body Diode)	$T_A= 25^\circ C$	--	--	0.5	A
V_{SD}	Forward on voltage②	$T_j=25^\circ C, I_{SD}=0.5A, V_{GS}=0V$	--	0.73	1.2	V

Notes:

1. Pulse width limited by maximum allowable junction temperature
2. Pulse test ; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Typical Characteristics

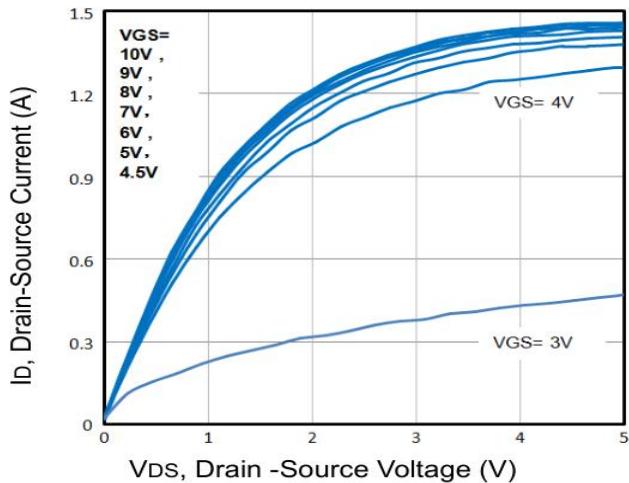


Fig1. Typical Output Characteristics

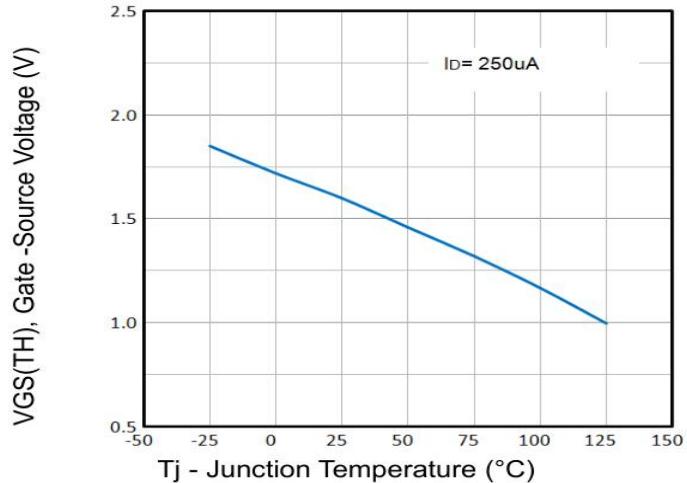


Fig2. Normalized Threshold Voltage Vs. Temperature

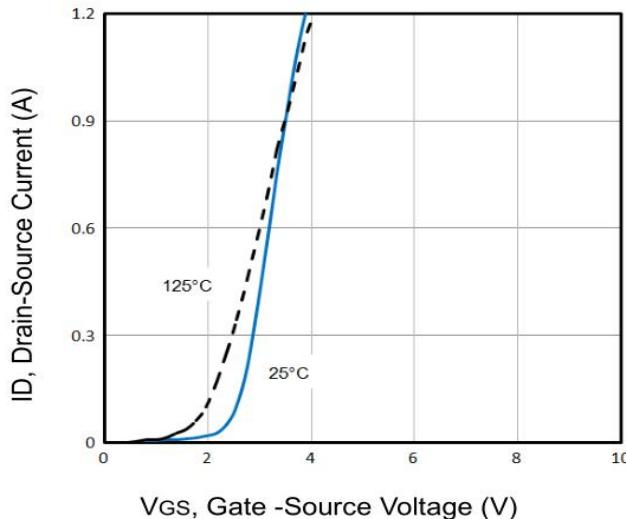


Fig3. Typical Transfer Characteristics

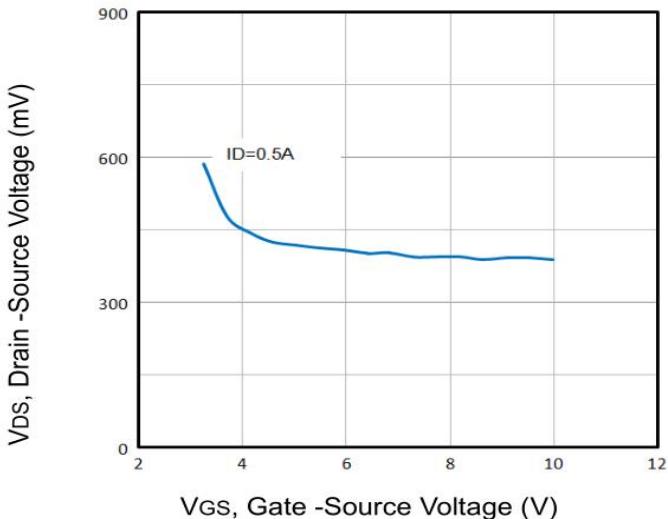


Fig4. Drain -Source Voltage vs Gate -Source Voltage

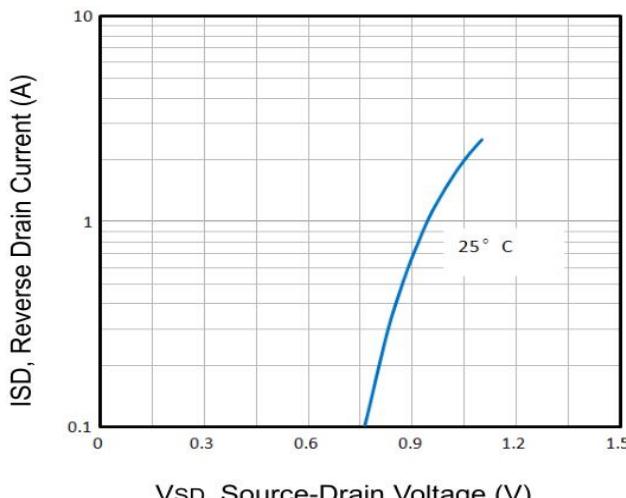


Fig5. Typical Source-Drain Diode Forward Voltage

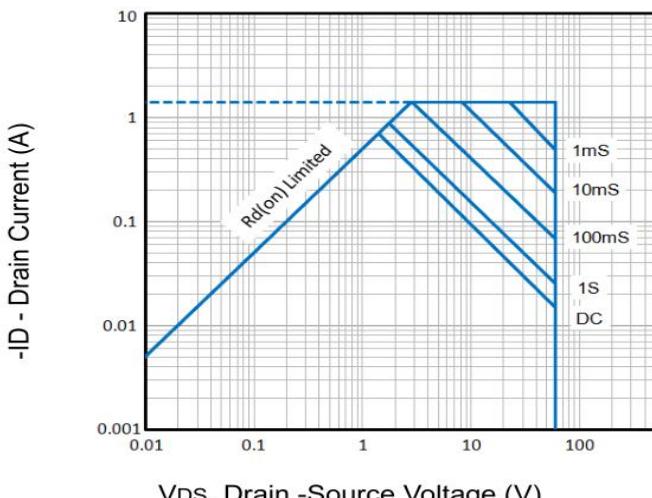


Fig6. Maximum Safe Operating Area

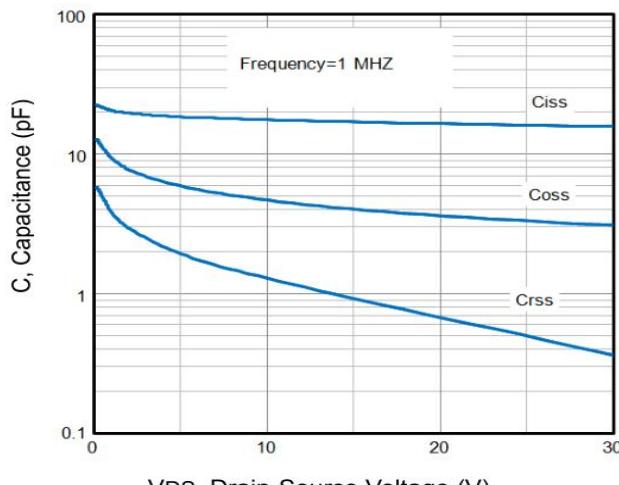


Fig7. Typical Capacitance Vs. Drain-Source Voltage

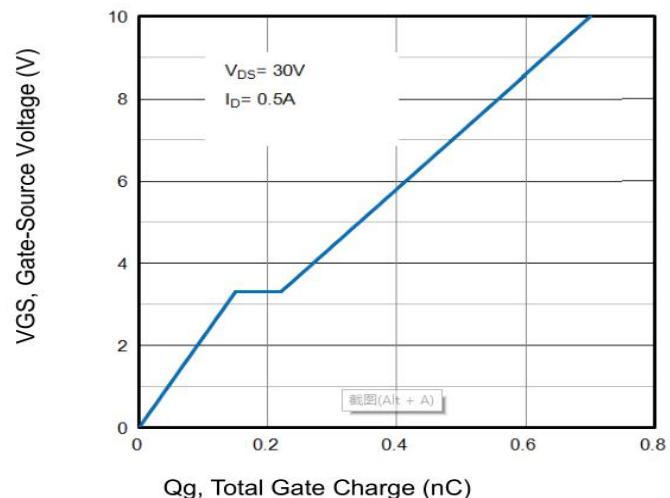


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

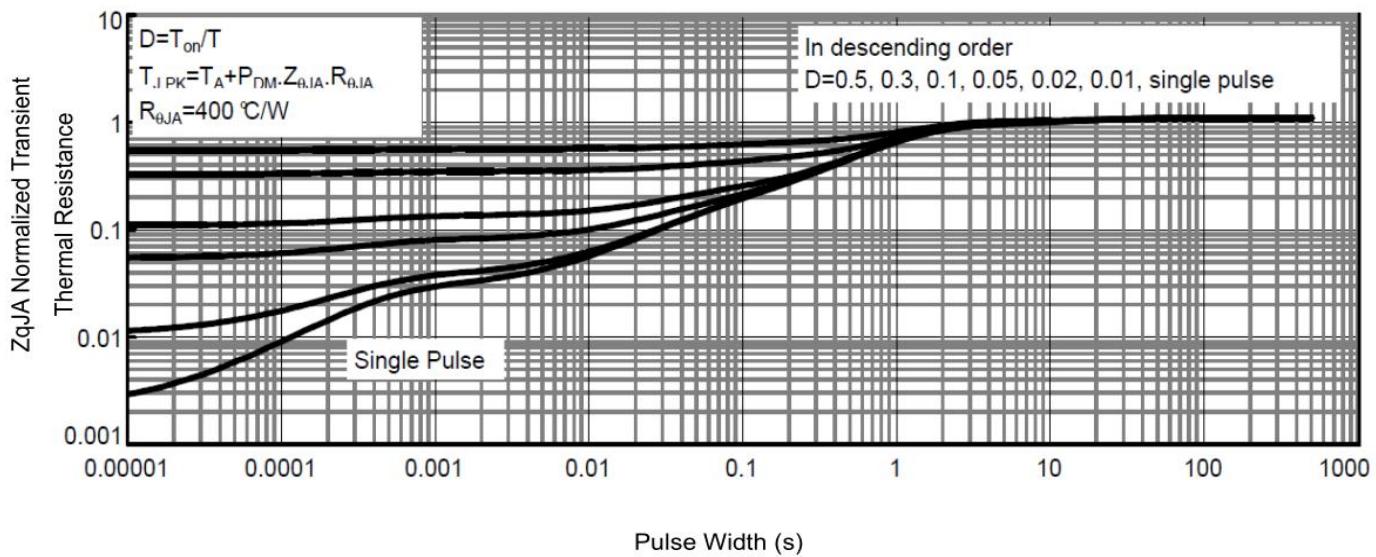


Fig9. Normalized Maximum Transient Thermal Impedance

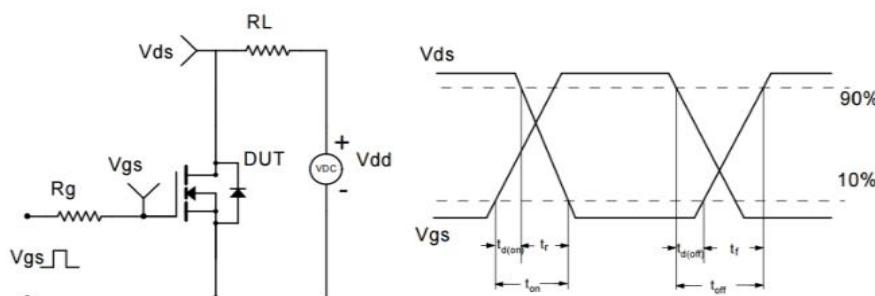
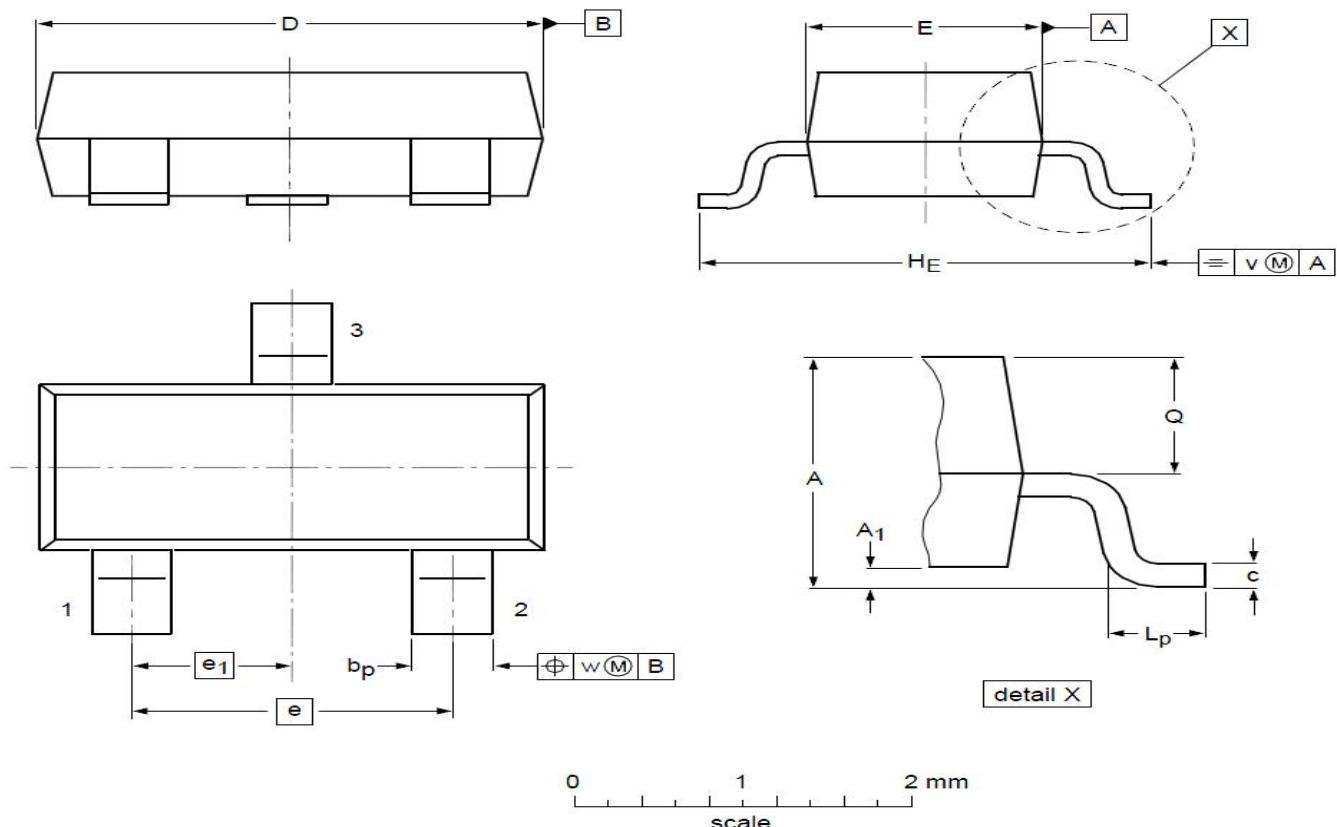


Fig10. Switching Time Test Circuit and waveforms

SOT23 Mechanical Data



SOT23 Mechanical Data

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A₁	0.01	0.05	0.10
b_p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e₁	--	0.95	--
H_E	2.25	2.40	2.55	L_p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				

Ordering information

Product	Package	Marking	Packing	Min Unit Quantity
2N7002K	SOT23	702K/72K	3000PCS/Reel	3000PCS