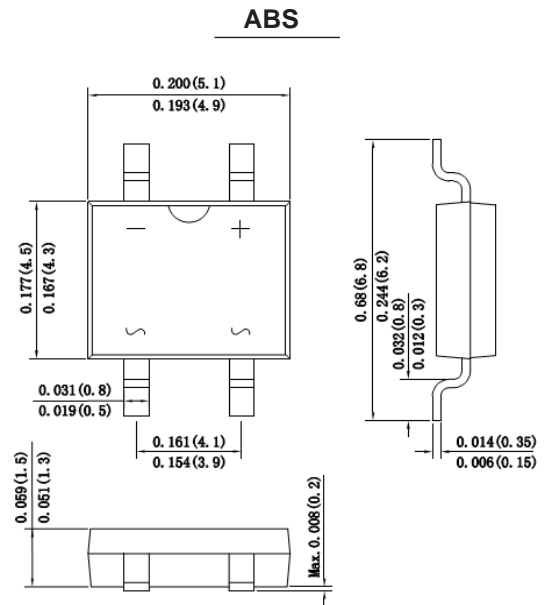


Features

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- Idea for printed circuit board
- Glass passivated Junction chip
- Low reverse leakage
- High forward surge current capability
- High temperature soldering guaranteed 250 °C /10 seconds at terminals

Mechanical Data

- Case: Molded plastic body
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity: Polarity symbol marking on body
- Mounting Position: Any
- Weight : 0.004 ounce, 0.12 grams



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

	SYMBOLS	ABS202	ABS204	ABS206	ABS208	ABS210	UNITS
Maximum repetitive peak reverse voltage	V _{RRM}	200	400	600	800	1000	VOLTS
Maximum RMS voltage	V _{RMS}	140	280	420	560	700	VOLTS
Maximum DC blocking voltage	V _{DC}	200	400	600	800	1000	VOLTS
Maximum average forward rectified current at T _L =30°C On glass-epoxy P.C.B (Note 1) On aluminum substrate (Note 2)	I _(AV)			1.5 2.0			Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}			50.0			Amps
Maximum instantaneous forward voltage at 2.0A	V _F			1.1			Volts
Maximum DC reverse current at T _A =25°C rated DC blocking voltage T _A =125°C	I _R			5.0 500			µA
Typical junction capacitance (Note 3)	C _J			15.0			pF
Typical thermal resistance	R _{θJA}			75.0			°C/W
Operating junction and storage temperature range	T _J , T _{STG}			-50 to +155			°C

- Note:**
1. Mounted on glass epoxy PC board with 1.3*1.3mm solder pad
 2. Mounted on aluminum substrate PC board with 1.3*1.3mm solder pad
 3. Measured at 1MHz and applied reverse voltage of 4.0V D.C.



FIG. 1- DERATING CURVE OUTPUT RECTIFIED CURRENT

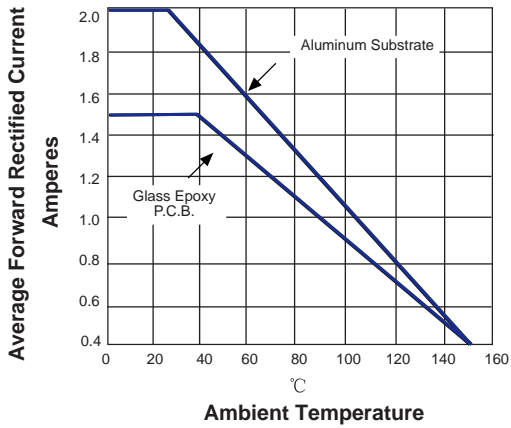


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT PER LEG

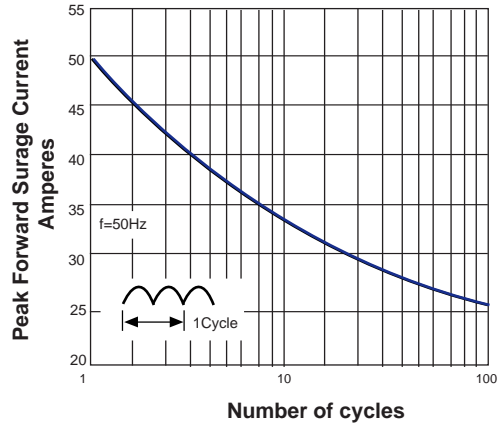


FIG. 3-TYPICAL FORWARD VOLTAGE CHARACTERISTICS

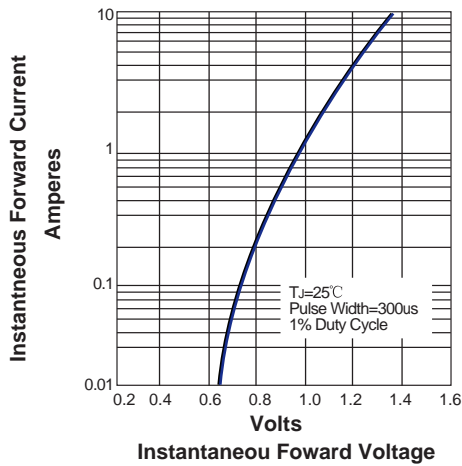


FIG. 4-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

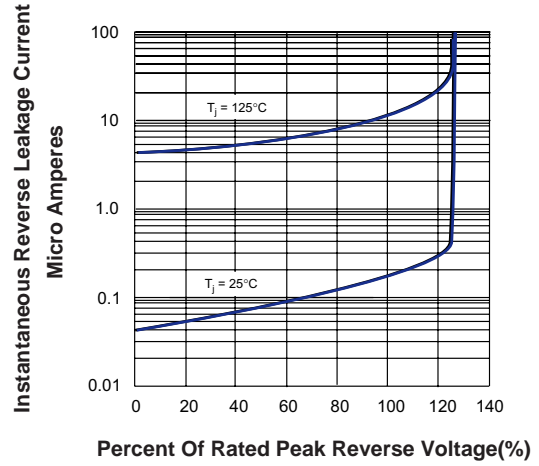


FIG. 5-TYPICAL JUNCTION CAPACITANCE

